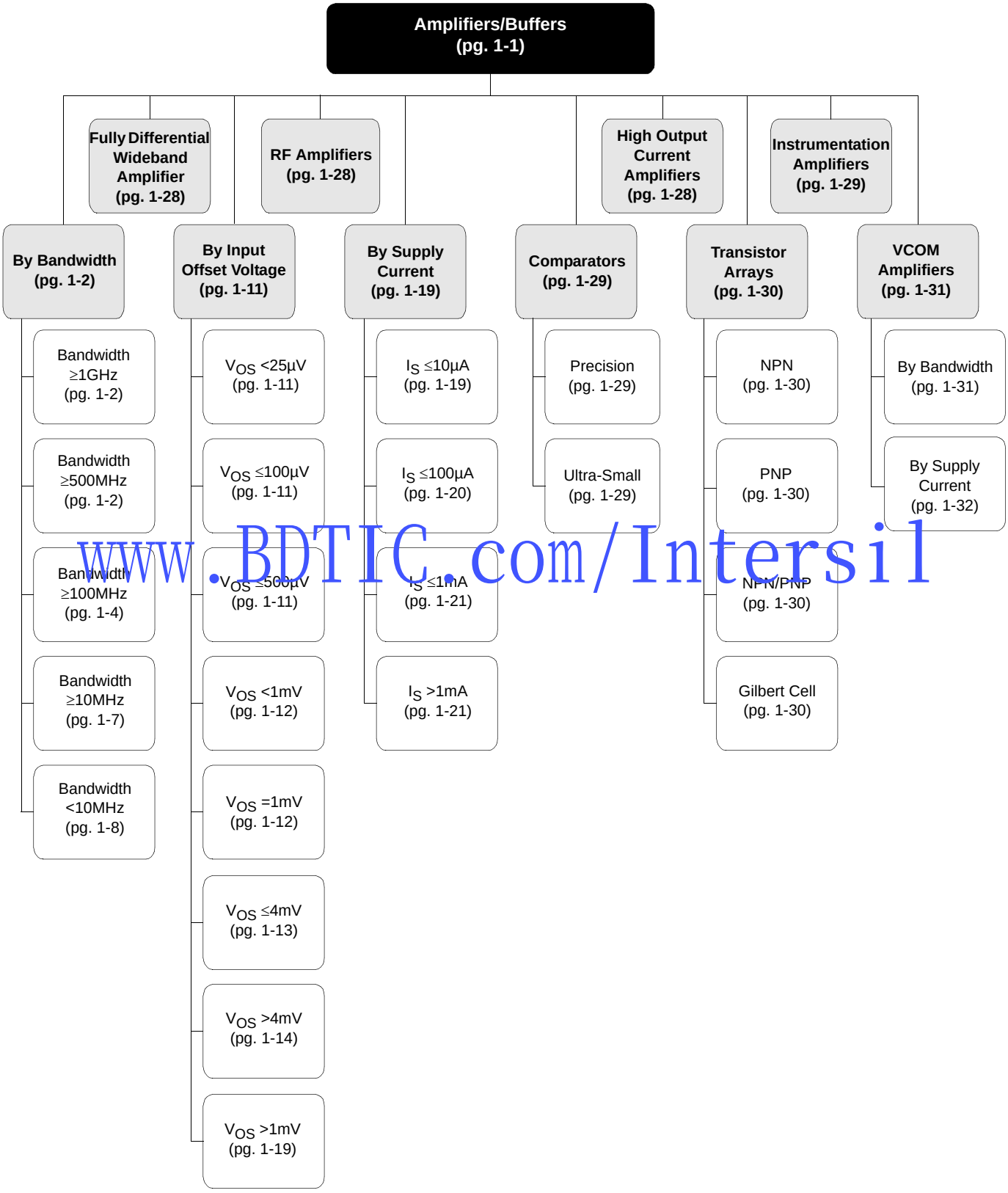
Amplifiers/Buffers (pg. 1-1)
Fully Differential Wideband Amplifier (pg. 1-28)
RF Amplifiers (pg. 1-28)
High Output Current Amplifiers (pg. 1-28)
Instrumentation Amplifiers (pg. 1-29)
By Bandwidth (pg. 1-2)
By Input Offset Voltage (pg. 1-11)
By Supply Current (pg. 1-19)
Comparators (pg. 1-29)
Transistor Arrays (pg. 1-30)
VCOM Amplifiers (pg. 1-31)
Bandwidth ≥1GHz (pg. 1-2)
Bandwidth ≥500MHz (pg. 1-2)
Bandwidth ≥100MHz (pg. 1-4)
Bandwidth ≥10MHz (pg. 1-7)
Bandwidth <10MHz (pg. 1-8)
VOS <25µV (pg. 1-11)
VOS ≤100µV (pg. 1-11)
VOS ≤500µV (pg. 1-11)
VOS <1mV (pg. 1-12)
VOS =1mV (pg. 1-12)
VOS ≤4mV (pg. 1-13)
VOS >4mV (pg. 1-14)
VOS >1mV (pg. 1-19)
IS ≤10µA (pg. 1-19)
IS ≤100µA (pg. 1-20)
IS ≤1mA (pg. 1-21)
IS >1mA (pg. 1-21)
Precision (pg. 1-29)
Ultra-Small (pg. 1-29)
NPN (pg. 1-30)
PNP (pg. 1-30)
NPN/PNP (pg. 1-30)
Gilbert Cell (pg. 1-30)
By Bandwidth (pg. 1-31)
By Supply Current (pg. 1-32)

www.BDTIC.com/Intersil

 Copyright © 2008 Intersil Americas Inc. All rights reserved.

By Bandwidth

Bandwidth ≥1GHz Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5166	Y	1400	6000	5	12	8.5	1.7	8.5	5	1	160	7.6	N	N	50	57	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5167	Y	1400	6000	5	12	8.5	1.7	8.5	5	1	160	7.6	N	N	50	57	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5191		1000	2800	5	10	9	3.8	40	15	1	120	7.4	N	N	75	50	1	Y	5 Ld SOT-23 T+R, 8 Ld SOIC
EL5191A		1000	2800	5	10	9	3.8	40	15	1	120	7.4	N	N	75	50	1	Y	8 Ld SOIC
EL5367		1000	6000	5	12	8.5	1.7	8.5	5	3	160	7.6	N	N	50	57	1	N	16 Ld QSOP

Bandwidth ≥500MHz Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
HFA1100	Y	850	2300	4.5	11	21	4	2.5	6	1	60	6.6	N	N	50	46	1	N	8 Ld PDIP, 8 Ld SOIC
HFA1112		850	2400	9	11	21	9	25	25	1	60	6.6	N	N	45		1	N	8 Ld PDIP, 8 Ld SOIC
HFA1113		850	2400	9	11	21	9	25	25	1	60	6.6	N	N	45		1	N	8 Ld SOIC
HFA1130		850	2300	9	11	21	4	25	6	1	60	6.6	N	N	50	46	1	N	20 Ld LCC, 8 Ld SOIC
ISL55190		800	268	3	5	16	1.2	25	0.5	1	130	4.97	N	Y	100	95	5	Y	8 Ld DFN, 8 Ld SOIC
ISL55191		800	260	3	5	6.1	1.3	12	0.8	1	132	4.98	N	Y	77	100	10	Y	8 Ld DFN, 8 Ld SOIC
ISL55290		800	268	3	5	16	1.2	25	0.5	2	130	4.97	N	Y	100	95	5	Y	10 Ld MSOP
ISL55291		800	260	3	5	6.1	1.3	12	0.8	2	132	4.98	N	Y	77	100	10	Y	10 Ld MSOP
HFA1110		750	1300	9	11	26	14	10	25	1	60	6.6	N	N	46	60	1	N	8 Ld SOIC
EL5104	Y	700	7000	5	10	9.5	10	8	10	1	160	7.6	N	N	70	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5105	Y	700	7000	5	10	9.5	10	8	10	1	160	7.6	N	N	70	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5178		700	850	4.75	11	12.5	18	14	30	1	100	7.4	N	N	75	78	2	N	8 Ld MSOP, 8 Ld SOIC
EL5204	Y	700	7000	5	10	9.5	10	8	10	2	160	7.6	N	N	70	62	1	Y	10 Ld MSOP
EL5205	Y	700	7000	5	10	9.5	10	8	10	2	160	7.6	N	N	70	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5304	Y	700	7000	5	10	9.5	10	8	10	3	160	7.6	N	N	70	62	1	Y	16 Ld QSOP
EL5378		700	1000	4.75	11	12.5	18	14	30	3	60	7.4	N	N	75	78	2	Y	28 Ld QSOP
EL5132	Y	670	1000	5	12	12	0.9	12	1	1	140	7	N	N	87	100	10	Y	8 Ld SOIC
EL5133	Y	670	1000	5	12	12	0.9	12	1	1	140	7	N	N	87	100	10	N	5 Ld SOT-23
EL5134	Y	650	450	5	12	6.7	1.5	3.7	1	1	140	7.8	N	N	85	108	5	Y	8 Ld SOIC
EL5135	Y	650	450	5	12	6.7	1.5	3.7	1	1	140	7.8	N	N	85	108	5	N	5 Ld SOT-23
EL5234	Y	650	450	5	12	6.7	1.5	3.7	1	2	140	7.8	N	N	85	108	5	Y	10 Ld MSOP
EL5235	Y	650	450	5	12	6.7	1.5	3.7	1	2	140	7.8	N	N	85	108	5	N	8 Ld SOIC
ISL59311		650	1500	4.5	5.5	17	15	6	1	3	60	4.4	N	N	75	75	2	Y	32 Ld QFN
EL5156	Y	600	700	5	12	6	12	0.4	1	1	140	7.6	N	N	90	108	1	Y	8 Ld SOIC
EL5157	Y	600	700	5	12	6	12	0.4	1	1	140	7.6	N	N	90	108	1	N	5 Ld SOT-23

www.BDTIC.com/Intersil

Bandwidth ≥500MHz Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5164	Y	600	4700	5	12	5	2.1	2	5	1	140	7.6	N	N	79	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5165	Y	600	4700	5	12	5	2.1	2	5	1	140	7.6	N	N	79	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5192	Y	600	2800	5	10	6	4.1	3	10	1	120	7.4	N	N	75	50	1	Y	5 Ld SOT-23
EL5192A		600	2800	5	10	6	4.1	3	10	1	120	7.4	N	N	75	50	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5256	Y	600	700	5	12	6	12	0.02	1	2	140	7.6	N	N	90	108	1	Y	10 Ld MSOP
EL5257		600	700	5	12	6	12	0.02	1	2	140	7.6	N	N	90	108	1	N	8 Ld MSOP, 8 Ld SOIC
EL5292	Y	600	2300	5	10	6	4.1	4	10	2	120	7.4	N	N	75	50	1	Y	8 Ld SOIC
EL5364	Y	600	4700	5	12	5	2.1	2	5	3	140	7.6	N	N	79	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5392A	Y	600	2300	5	10	6	4.1	4	10	3	120	7.4	N	N	75	50	1	Y	16 Ld QSOP
HA-2539	Y	600	600	20	30	20	6	5	15	1	20	20	N	N	70	72	10	N	14 Ld PDIP
HFA1305		560	2500	9	11	5.8	3.5	6	5	3	60	6.6	N	N	52	48	1	N	14 Ld SOIC
HFA1405		560	2500	9	11	5.8	3.5	6	5	4	60	6.6	N	N	52	48	1	N	14 Ld PDIP, 14 Ld SOIC
EL5174		550	1100	4.75	11	12.5	21	14	25	1	60	7.6	N	N	75	78	1	N	8 Ld SOIC
EL5175	Y	550	600	4.75	11	9.6	21	12.5	40	1	67	7.49	N	N	56	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5177		550	1100	4.75	11	12.5	21	14	25	1	50	7.6	N	N	75	78	1	Y	10 Ld MSOP
EL5374		550	1100	4.75	11	12.5	21	14	25	3	60	7.6	N	N	75	78	1	Y	28 Ld QSOP
EL5375		550	900	4.75	11	9.6	21	12.5	30	3	67	7.49	N	N	56	95	1	Y	24 Ld QSOP
EL5130	Y	500	350	5	12	4	1.8	2.3	0.9	1	100	7.6	N	N	90	110	5	Y	8 Ld SOIC
EL5131	Y	500	350	5	12	4	1.8	2.3	0.9	1	100	7.6	N	N	90	110	5	N	5 Ld SOT-23
EL5162	Y	500	4000	5	12	1.5	3	2	5	1	100	7.2	N	N	76	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5163	Y	500	4000	5	12	1.5	3	2	5	1	100	7.2	N	N	76	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5262	Y	500	4000	5	12	1.5	3	2	5	2	100	7.2	N	N	76	62	1	Y	10 Ld MSOP
EL5263	Y	500	4000	5	12	1.5	3	2	5	2	100	7.2	N	N	76	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5362	Y	500	4000	5	12	1.5	3	2	5	3	100	7.2	N	N	76	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5462		500	4000	5	12	1.5	3	2	1.5	4	100	7.2	N	N	76	62	1	N	14 Ld SOIC
EL8102	Y	500	600	3	5	5.6	12	6	8	1	65	4.8	N	Y	95	95	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL8103	Y	500	600	3	5	5.6	12	6	8	1	65	4.8	N	Y	95	95	1	N	5 Ld SOT-23
EL8202	Y	500	600	3	5.5	5.6	12	6	8	2	65	4.8	N	Y	95	95	1	Y	10 Ld MSOP
EL8203	Y	500	600	3	5.5	5.6	12	6	8	2	65	4.8	N	Y	95	95	1	N	8 Ld MSOP, 8 Ld SOIC
EL8302	Y	500	600	3	5.5	5.6	12	6	7	3	65	4.85	N	Y	95	95	1	Y	16 Ld QSOP, 16 Ld SOIC
EL8403	Y	500	600	3	5.5	5.6	12	6	8	4	65	4.8	N	Y	95	85	1	N	14 Ld SOIC, 16 Ld QSOP

www.BDTIC.com/Intersil

 Copyright © 2008 Intersil Americas Inc. All rights reserved.

Bandwidth ≥100MHz Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5108		450	4500	5	12	3.5	2	2	5	1	135	7.6	N	N	75		1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5173		450	900	5	11	12	25	11	30	1	55	7.7	N	N	73	80	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5308	Y	450	4500	5	12	3.5	2	2	5	3	135	7.6	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
EL5373		450	1100	4.75	11	12	25	13	30	3	55	7.7	N	N	73	80	2	Y	24 Ld QSOP
HFA1109	Y	450	1100	9	11	10	4	4	5	1	36	6.4	N	N	53	50	1	N	8 Ld SOIC
EL5102	Y	400	2200	5	10	5.2	12	2	5	1	150	7.8	N	N	80	80	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5103	Y	400	2200	5	10	5.2	6	2	5	1	150	7.8	N	N	80	80	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5202	Y	400	2200	5	10	5.2	12	2	5	2	150	7.8	N	N	80	80	1	Y	10 Ld MSOP
EL5203	Y	400	2200	5	10	5.2	12	2	5	2	150	7.8	N	N	80	80	1	N	8 Ld MSOP, 8 Ld SOIC
EL5302	Y	400	2200	5	10	5.2	12	2	5	3	150	7.8	N	N	80	80	1	Y	16 Ld QSOP
EL5396A		400	2600	5	10	9	3.8	40	15	3	120	7.4	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
HA-2540	Y	400	400	20	30	20	6	5	8	1	20	20	N	N	70	72	10	N	14 Ld CerDIP
ISL55036		400	2500	3	5.5	7.2	12.5	5.5	10	6	95	4.86	N	Y	78		4	Y	24 Ld TQFN
HFA1135		360	1200	9	11	6.9	3.5	6	5	1	60	6.8	N	N	54	50	1	N	8 Ld SOIC
HFA1155		360	1650	4.5	11	5.5	4.7	25	6	1	55	6.6	N	N	50	46	1	N	5 Ld SOT-23 T+R
EL1516		350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	N	8 Ld MSOP, 8 Ld SOIC
EL1516A		350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	Y	10 Ld MSOP
EL4543		350	1200	5	12	14.5	27	15	10	3	60		N	N	80	80	2	Y	Eval Board, 20 Ld QFN, 24 Ld QSOP
EL5106	Y	350	4500	5	12	1.5	2.8	1.5	10	1	100	7.2	N	N	75		1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5306	Y	350	4500	5	12	1.5	2.8	1.5	10	3	100	7.2	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
HFA1412		350	1650	9	11	5.9	7	1	10	4	55	6.4	N	N	49	45	1	N	14 Ld PDIP, 14 Ld SOIC
HFA1105		330	1000	9	11	5.8	3.5	2	5	1	60	6.8	N	N	54	50	1	N	8 Ld SOIC
HFA1145		330	1000	9	11	5.9	3.5	6	6	1	60	6.8	N	N	54	50	1	Y	8 Ld PDIP, 8 Ld SOIC
EL5193	Y	300	2600	5	10	4	4.4	1	10	1	120	7.4	N	N	75	50	1	Y	5 Ld SOT-23 T+R, 6 Ld SOT-23 T+R
EL5193A		300	2600	5	10	4	4.4	1	10	1	120	7.4	N	N	75	50	1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
EL5293	Y	300	2200	5	10	4	4.4	1	10	2	120	7.4	N	N	75	50	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5393A	Y	300	2200	5	10	4	4.4	1	10	3	120	7.4	N	N	75	50	1	Y	16 Ld QSOP, 16 Ld SOIC
EL8108		300	1100	4.5	13	14.3	6	5	25	2	450	10	N	N	90		1	Y	16 Ld QFN, 8 Ld SOIC
ISL1557		300	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

Bandwidth ≥100MHz Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ISL1557A		300	1200	4.5	13.2	15	8.2	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN
ISL55020		300	1200	4.5	12	21	6.9	7	38	1	150	10	N	N	60		1	Y	16 Ld QFN T+R
EL5152	Y	270	180	5	12	3	12	0.12	1	1	105	7.4	N	N	116	110	1	Y	8 Ld SOIC
EL5153	Y	270	180	5	12	3	12	0.12	1	1	105	7.4	N	N	116	110	1	N	5 Ld SOT-23
EL5252	Y	270	180	5	12	3	12	0.11	1	2	105	7.4	N	N	95	110	1	Y	10 Ld MSOP
EL5455	Y	270	180	5	12	3	12	0.12	1	4	105	7.4	N	N	96	110	1	N	14 Ld SOIC
EL2480	Y	250	1200	3	12	3	3	15	10	4	55	8	N	N	70	50	1	N	14 Ld SOIC
EL5171		250	800	5	10	8	26	6	25	1	90	6.8	N	N	84	82	1	N	8 Ld SOIC
EL5172		250	800	5	10	6	26	6	25	1	95	7.5	N	N	58	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5176		250	800	4.75	11	7.5	26	6	2.5	1	40	7.8	N	N	84	82	1	Y	10 Ld MSOP
EL5371		250	800	5	10	8	26	6	25	3	90	6.8	N	N	84	82	1	N	28 Ld QSOP
EL5372		250	800	5	10	6	26	6	25	3	95	7.5	N	N	58	95	1	Y	24 Ld QSOP
ISL1571		250	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95		1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1572		250	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95		1	Y	10 Ld HMSOP, 16 Ld QFN
ISL55001		220	300	5	30	9	12	0.6	5	1	140	26.15	N	N	100	90	1	N	8 Ld SOIC
EL1517B		200	800	5	12	7	6	5	25	2	250	10	N	N	90		1	Y	10 Ld HMSOP, 16 Ld QFN, 8 Ld SOIC
EL5100	Y	200	2200	5	10	2.5	10	2	4	1	100	6.8	N	N	90	75	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5101	Y	200	2200	5	10	2.5	10	2	4	1	100	6.8	N	N	90	75	1	N	5 Ld SOT-23
EL5150	Y	200	67	5	12	1.4	12	0.02	1	1	70	5.6	N	N	110	100	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5151	Y	200	67	5	12	1.4	12	0.02	1	1	70	5.6	N	N	110	100	1	N	5 Ld SOT-23
EL5160	Y	200	1700	5	10	0.75	4	5	5	1	70	6.8	N	N	74	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5161	Y	200	1700	5	10	0.75	4	5	5	1	70	6.8	N	N	74	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5197	Y	200	2200	5	10	4	4.4	1	10	1	120	7.4	N	N	75		1	Y	5 Ld SOT-23
EL5197A		200	2200	5	10	4	4.4	1	10	1	120	7.4	N	N	75		1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
EL5250	Y	200	67	5	12	1.4	12	0.02	1	2	70	5.6	N	N	110	100	1	Y	10 Ld MSOP
EL5251	Y	200	67	5	12	1.4	12	0.02	1	2	70	5.6	N	N	110	100	1	N	8 Ld MSOP, 8 Ld SOIC
EL5260	Y	200	1700	5	10	0.75	4	5	5	2	70	6.8	N	N	74	62	1	Y	10 Ld MSOP
EL5261	Y	200	1700	5	10	0.75	4	5	5	2	70	6.8	N	N	74	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5300	Y	200	2200	5	10	2.5	10	2	4	3	100	6.8	N	N	90	75	1	Y	16 Ld QSOP
EL5360	Y	200	1700	5	10	0.75	4	5	5	3	70	6.8	N	N	74	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5397A		200	2100	5	10	4	4.8	1	10	3	120	7.4	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
EL5451	Y	200	67	5	12	1.4	12	0.02	1	4	70	5.6	N	N	110	100	1	N	14 Ld SOIC
EL8100	Y	200	200	3	5	2	10	1.5	6	1	65	4.8	N	Y	100	90	1	Y	6 Ld SOT-23, 8 Ld SOIC

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

Bandwidth ≥100MHz Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL8101	Y	200	200	3	5	2	10	1.5	6	1	65	4.8	N	Y	100	90	1	N	5 Ld SOT-23
EL8200	Y	200	200	3	5.5	2	10	1.6	6	2	65	4.8	N	Y	100	90	1	Y	10 Ld MSOP
EL8201	Y	200	200	3	5.5	2	10	1.6	6	2	65	4.8	N	Y	100	90	1	N	8 Ld SOIC
EL8300	Y	200	200	3	5.5	2	10	1.4	5	3	65	4.83	N	Y	100	90	1	Y	16 Ld QSOP, 16 Ld SOIC
EL8401	Y	200	200	3	5.5	2	10	1.6	6	4	65	4.8	N	Y	100	90	1	N	14 Ld SOIC, 16 Ld QSOP
ISL55002		200	300	5	30	8.5	12	0.6	5	2	140	26.15	N	N	100	90	1	N	8 Ld SOIC
ISL55004		200	300	5	30	8.5	12	0.6	5	4	140	26.15	N	N	100	90	1	N	14 Ld SOIC
ISL59830		200	500	3	3.6	40	20		25	3	50	5.1	Y	Y	90		2	Y	16 Ld QSOP
ISL59830A		200	500	3	3.6	40	20		25	3	50	5.1	Y	Y	90		2	Y	16 Ld QSOP
ISL59833		200	500	3	3.6	32.3	20		25	3	50	5.1	Y	Y	68		2	Y	16 Ld QSOP
ISL59837		200	500	3	3.6	32.3	20		25	3	50	5.1	Y	Y	62		2	Y	16 Ld QSOP
EL2125	Y	175	185	5	30	10	0.83	22	2	1	100	27.1	N	N	97	106	10	N	5 Ld SOT-23, 8 Ld SOIC
ISL28190		170	50	3	5.5	11	1	16	0.7	1	144	4.99	N	Y	80	103	1	Y	6 Ld SOT-23 T+R, 6 Ld µTDFN T+R
ISL28290		170	50	3	5.5	11	1	16	0.7	2	144	4.97	N	Y	80	103	1	Y	10 Ld MSOP, 10 Ld µTQFN T+R, 8 Ld SOIC
EL2141		150	800	6	12.6	11	36	6	40	1	60	7.2	N	N	70		2	N	8 Ld SOIC
EL2142		150	400	6	12.6	11	36	6	10	1	60	7.2	N	N	70	70	1	N	8 Ld SOIC
EL9110		130	1500	10	12	33		1	250	1	60	7	N	N	60	60	1	Y	16 Ld QSOP
EL2250		125	300	2.7	12	5	48	5.5	12	2	100	10.8	N	N	70	70	1	N	8 Ld SOIC
HA5023	Y	125	475	9	30	7.5	4.5	4	3	2	20	6	N	N	60	53	1	N	8 Ld PDIP, 8 Ld SOIC
HA5024	Y	125	475	9	30	7.5	4.5	4	3	4	20	6	N	N	60	53	1	Y	20 Ld PDIP, 20 Ld SOIC
HA-5147		120	35	10	40	3.5	3.2	0.015	0.03	1	25	27	N	N	96	120	10	N	8 Ld CerDIP
EL2227	Y	115	50	5	24	4.8	1.9	3.4	3	2	100	20.8	N	N	95	94	2	N	8 Ld MSOP, 8 Ld SOIC
EL2045	Y	100	275	4	36	5.2	15	2.8	7	1	75	27.2	N	N	85	95	2	N	8 Ld PDIP, 8 Ld SOIC
EL5144		100	200	4.75	5.25	7	25	2	25	1	90	4.85	N	Y	60	50	1	N	5 Ld SOT-23
EL5146		100	200	4.75	5.25	7	25	2	25	1	90	4.85	N	Y	60	50	1	Y	8 Ld PDIP, 8 Ld SOIC
EL5170		100	1100	5	10	7.4	30	6	25	1	85	6.9	N	N	83	84	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5244		100	200	4.75	5.25	7	25	2	25	2	90	4.85	N	Y	60	50	1	N	8 Ld MSOP, 8 Ld PDIP, 8 Ld SOIC
EL5246		100	200	4.75	5.25	7	25	2	25	2	90	4.85	N	Y	60		1	Y	10 Ld MSOP, 14 Ld PDIP, 14 Ld SOIC
EL5370		100	1100	5	10	7	30	6	25	3	85	6.9	N	N	83	84	2	Y	24 Ld QSOP
EL5444		100	200	4.75	5.25	7	25	2	15	4	90	4.85	N	Y	60	50	1	N	14 Ld PDIP, 14 Ld SOIC, 16 Ld QSOP
HA-2620	Y	100	35	10	40	3	11	0.001	0.5	1	22	24	N	N	90	100	5	N	8 Ld Can

www.BDTIC.com/Intersil

Bandwidth ≥100MHz Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
HA-2625	Y	100	35	10	40	3	11	0.005	3	1	18	24	N	N	90	100	5	N	8 Ld PDIP, 8 Ld SOIC
HA-5020	Y	100	800	10	30	10	4.5	3	8	1	20	25.4	N	N	64	60	1	Y	20 Ld LCC, 8 Ld PDIP, 8 Ld SOIC
HA-5160		100	120	14	36	8	35	0.00002	3	1	20	22	N	N	86	80	1	N	8 Ld Can
ISL1535		100	500	5	30	13.3	3.9	70		2	500	21.6	N	N	50		5	Y	24 Ld QFN, 28 Ld HTSSOP

Bandwidth ≥100MHz - Buffers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	Gain A_V (min)	Enable	Package
HA4600		480	1700	9	11	10.5		30	10	1	20	5.6	N	N		1	Y	6 Ld SOT-23, 8 Ld PDIP, 8 Ld SOIC, 8 Ld SOT-23 T+R
HA-5033	Y	250	1100	10	32	21	20	20	5	1	100	22	N	N	54		N	12 Ld Can, 8 Ld PDIP
HA-5002	Y	110	1300	10	40	8.3		2	5	1	200	27	N	N	64	0.995	N	20 Ld PLCC, 8 Ld Can, 8 Ld PDIP, 8 Ld SOIC

Bandwidth ≥10MHz Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL2126	Y	90	110	5	30	4.5	1.3	7	2	1	100	27.5	N	N	100	100	10	N	5 Ld SOT-23, 8 Ld SOIC
EL1503A		80	1100	10	24	12.5	3.5	50	30	2	1000	21.2	Y	N	90		1	Y	20 Ld SOIC, 24 Ld QFN
EL1508		80	600	5	12	3.5	3.5	50	10	2	1000	21.6	N	N			1	Y	16 Ld SOIC, 20 Ld SOIC, 24 Ld QFN
EL2228		80	65	5	24	4.5	4.9	4.5	3	2	180	20.6	N	N	83	90	1	N	8 Ld MSOP, 8 Ld SOIC
EL1506		70	500	5	24	3.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	24 Ld SOIC, 28 Ld HTSSOP
EL1507		70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	16 Ld QFN, 16 Ld SOIC
EL1509		70	500	5	12	7	2.8	30	20	2	450	10	N	N	50		1	N	8 Ld DFN, 8 Ld SOIC
EL1510		70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	62		1	N	8 Ld DFN, 8 Ld SOIC
EL1511		70	500	5	15	3.5	2.8	30	20	2	450	13	N	N	50		1	Y	16 Ld QFN, 16 Ld SOIC
EL1519		70	350	5	12	7.5	2.7	30	20	2	450	10	N	N	50		1	N	8 Ld SOIC
EL1527		70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	60		1	Y	28 Ld HTSSOP
EL2276		70	800	3	12.6	1	5.1	15	15	2	55	8	N	N	70	50	1	Y	14 Ld SOIC
HA-5137A	Y	63	20	10	40	3.5	3	0.01	0.01	1	3.5	27.6	N	N	114	126	5	N	8 Ld CerDIP
ISL28191		61	17	3	5.5	3.5	1.7	6	0.63	1	130	4.97	N	Y	80	100	1	Y	6 Ld SOT-23 T+R, 6 Ld µTDFN T+R
ISL28291		61	17	3	5.5	3.5	1.7	6	0.63	2	130	4.97	N	Y	80	100	1	Y	10 Ld MSOP, 10 Ld µTQFN T+R, 8 Ld SOIC

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

Bandwidth ≥10MHz Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL1537		50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537A		50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537B		50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1526		46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	20 Ld HTSSOP, 24 Ld QFN
EL1529		46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	24 Ld QFN
EL1528		40	210	10	24	5	3.5	30	10	2	1000	22.2	N	N	75		1	Y	20 Ld HTSSOP, 24 Ld QFN
HA-2404		40	30	20	40	4.8	20	0.05	4	4	20	24	N	N	90	100	1	Y	16 Ld CerDIP
ISL59112		40	85	2.5	3.6	2		5		1	40	3.2	N	N	63		6	N	6 Ld SC-70
HA-2520	Y	20	120	16	36	4	20	0.1	8	1	10	24	N	N	90	90	3	N	8 Ld Can, 8 Ld CerDIP
HA-2522	Y	20	120	16	36	4	20	0.125	10	1	10	24	N	N	90	90	3	N	8 Ld Can
HA-2525	Y	20	120	16	36	4	20	0.125	10	1	10	24	N	N	90	90	3	N	8 Ld Can, 8 Ld CerDIP, 8 Ld PDIP, 8 Ld SOIC
CA3130		15	30	5	16	2	23000	0.000005	8	1	22	13.3	N	N	90	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
CA3130A		15	30	5	16	2	23000	0.000005	2	1	22	13.3	N	N	90	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
HA-2510/883		12	50	20	40	6		0.2	8	1	65	20	N	N	80	80	1	N	8 Ld Can, 8 Ld CerDIP
HA-2600	Y	12	7	10	40	3	11	0.001	4.5	1	22	24	N	N	90	100	1	N	8 Ld Can, 8 Ld CerDIP

www.BDTIC.com/Intersil

Bandwidth ≥10MHz - Buffers

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	Gain A_V (min)	Enable	Package
EL5123	12	15	4.5	16.5	0.5	9.5	0.002	12	4	120	14.9	Y	Y	80	1	N	10 Ld MSOP
EL5221	12	10	4.5	16.5	0.5	10	0.002	14	2	120	14.9	Y	Y	80		N	6 Ld SOT-23 T+R, 8 Ld MSOP
EL5223	12	15	4.5	16.5	0.5	9.5	0.002	12	8	120	14.9	Y	Y	80		N	20 Ld TSSOP, 24 Ld QFN
EL5323	12	15	4.5	16.5	0.5	9.5	0.002	12	10	120	14.9	Y	Y	80	1	N	24 Ld QFN, 24 Ld TSSOP
EL5421	12	10	4.5	16.5	0.5	10	0.002	14	4	120	14.9	Y	Y	80	1	N	10 Ld MSOP
EL5423	12	15	4.5	16.5	0.5	9.5	0.002	12	12	120	14.9	Y	Y	80	1	N	28 Ld TSSOP, 32 Ld QFN
EL5623	10	9	4.5	18	0.65	16	0.002	20	6	120	14.9	Y	Y	80	1	N	16 Ld TSSOP

Bandwidth <10MHz - Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
HA-5127	Y	8.5	10	10	30	3.5	3	0.015	0.01	1	25	27	N	N	96	120	1	N	8 Ld SOIC
HA-5127A	Y	8.5	10	10	30	3.5	3	0.01	0.01	1	25	27	N	N	114	126	1	N	8 Ld CerDIP
HA-5102	Y	8	3	5	18	3	4.3	130	0.5	2	15	26	N	N	100	95	1	N	8 Ld Can, 8 Ld CerDIP

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

Bandwidth <10MHz - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
HA-5104	Y	8	3	5	18	5	4.3	130	0.5	4	15	26	N	N	100	95	1	N	14 Ld CerDIP, 16 Ld SOIC, 20 Ld LCC
ISL59110		8	40	2.5	3.6	2				1	115	3.2	N	Y	90		2	Y	6 Ld SC-70
ISL59111		8	40	2.5	3.6	2				1	115	3.2	N	Y	90		2	Y	6 Ld CSP
ISL28136		5	1.9	2.4	5.5	0.9	15	0.016	0.01	1	56	4.99	Y	Y	99	114	1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
ISL28146		5	1.9	2.4	5.5	0.95	12	0.016	0.4	1	56	4.99	Y	Y	99	114	1	Y	6 Ld SOT-23 T+R
CA3140		4.5	9	4	36	4	12	0.00001	5	1	40	27.4	N	N	80	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
CA3140A		4.5	9	4	36	4	12	0.00001	2	1	40	27.4	N	N	80	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
CA3240		4.5	9	4	36	4	12	0.00001	5	2	40	27.4	N	N	80	90	1	N	8 Ld PDIP
CA3240A		4.5	9	4	36	4	12	0.00001	2	2	40	27.4	N	N	80	90	1	N	8 Ld PDIP
ISL28138		4.5	4	2.4	5.5	0.9	26	0.000001	0.3	1	75	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23, 8 Ld SOIC
ISL28148		4.5	4	2.4	5.5	1.1	28	0.000001	1.8	1	75	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23 T+R, 6 Ld WLCSP T+R
CA3260		4	10	4	16	1.2	25	0.000002	6	2	22	15	N	N	90	90	1	N	8 Ld PDIP
CA3260A		4	10	4	16	1.2	25	0.000002	2	2	22	15	N	N	90	95	1	N	8 Ld PDIP
HA-2640		4	5	20	80	3.2	15	0.01	4	1	15	70	N	N	90	100	1	N	8 Ld Can, 8 Ld CerDIP
HA-2645		4	5	20	80	3.2	15	0.012	6	1	12	70	N	N	90	100	1	N	8 Ld Can, 8 Ld CerDIP
CA5260		3	5	4.5	16	1.8	25	0.000002	6	2	22	15	N	Y	84	85	1	N	8 Ld SOIC
CA5260A		3	5	4.5	16	1.8	25	0.000002	2	2	22	15	N	Y	84	85	1	N	8 Ld SOIC
ICL7611		1.4	1.6	2	16	0.01	100	0.001	4	1	40	9.8	N	N	94	96	1	N	8 Ld PDIP, 8 Ld SOIC
ICL7612		1.4	1.6	2	16	0.01	100	0.001	4	1	40	9.8	N	N	94	96	1	N	8 Ld PDIP, 8 Ld SOIC
CA3420		0.5	0.5	2	20	0.45	38	0.000005	5	1	1.5	19.75	N	Y	80	65	1	Strobe	8 Ld PDIP
CA5420A		0.5	0.5	2	20	0.45	38	0.000002	2	1	2.7	19.75	Y	N	83	83	1	Strobe	8 Ld SOIC
ICL7621		0.48	0.16	2	16	0.1	100	0.001	4	2	1	9.8	N	N	86	91	1	N	8 Ld PDIP, 8 Ld SOIC
EL8171		0.45	0.5	2.9	5	0.078	200	0.00001	1	1	29	5.5	Y	Y	90	104	10	Y	8 Ld SOIC
EL8176		0.4	0.13	2.4	5.5	0.055	28	0.0005	0.1	1	31	4.994	Y	Y	110	110	1	Y	6 Ld SOT-23 T+R, 6 Ld WLCSP T+R, 8 Ld SOIC
ISL28276		0.4	0.2	2.4	5	0.06	30	0.00005	0.1	2	30	4.99	Y	Y	115	115	1	Y	16 Ld QSOP, 8 Ld SOIC
ISL28286		0.4	0.17	2.4	5	0.06	50	0.0005	0.6	2	30	4.99	Y	Y	115	115	1	Y	10 Ld MSOP
ISL28476		0.4	0.2	2.4	5	0.06	30	0.0005	0.1	4	30	4.99	Y	Y	115	115	1	N	16 Ld QSOP
ISL28486		0.4	0.17	2.4	5	0.06	50	0.0005	0.6	4	26	4.99	Y	Y	115	115	1	N	16 Ld QSOP
EL8173		0.396	0.5	2.9	5	0.078	200	0.0007	1	1	29	5.5	Y	Y	90	104	10	Y	8 Ld SOIC
ISL28278		0.3	0.14	2.4	5	0.06	45	0.00001	0.225	2	26	4.99	Y	Y	105	100	1	Y	16 Ld QSOP
ISL28288		0.3	0.14	2.4	5	0.06	48	0.00001	1.5	2	31	4.99	Y	Y	105	100	1	Y	10 Ld MSOP, 8 Ld SOIC
ISL28478		0.3	0.14	2.4	5	0.06	45	0.00001	0.225	4	26	4.99	Y	Y	105	100	1	N	16 Ld QSOP

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

Bandwidth <10MHz - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ISL28488		0.3	0.14	2.4	5	0.06	48	0.00001	1.5	4	31	4.99	Y	Y	105	100	1	N	14 Ld TSSOP, 16 Ld QSOP
EL8178		0.266	0.15	2.4	5.5	0.055	48	0.000025	1.5	1	31	4.994	Y	Y	100	100	1	Y	6 Ld SOT-23 T+R, 6 Ld WLCSP T+R, 8 Ld SOIC
EL8188		0.266	0.15	2.4	5	0.055	48	0.000075	1.5	1	31	4.994	Y	Y	100	100	1	Y	6 Ld SOT-23, 8 Ld SOIC
ISL28156		0.25	0.05	2.4	5.5	0.039	46	0.005	0.4	1	31	4.99	Y	Y	104	110	1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
ISL28158		0.25	0.1	2.4	5.5	0.034	64	0.000001	0.65	1	30	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23, 8 Ld SOIC
ISL28166		0.25	0.05	2.4	5	0.039	46	0.005	0.6	1	31	4.98	Y	Y	104	110	1	Y	6 Ld SOT-23 T+R
ISL28270		0.24	0.5	2.4	5.5	0.06	60	0.0005	0.15	2	29	4.99	Y	Y	110	110	100	Y	16 Ld QSOP
ISL28470		0.24	0.5	2.4	5.5	0.065	60	0.00025	0.015	4	29	4.99	Y	Y	110	110	100	Y	28 Ld QSOP
ISL28273		0.23	0.6	2.4	5.5	0.06	210	0.0002	0.6	2	29	4.99	Y	Y	95	110	10	Y	16 Ld QSOP
ISL28168		0.2	0.1	2.4	5.5	0.034	64	0.00001	1.6	1	30	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23
EL8170		0.192	0.5	2.9	5	0.078	50	0.0007	0.25	1	29	5.5	Y	Y	104	108	100	Y	8 Ld SOIC
ISL28271		0.18	0.5	2.4	5	0.078	240	0.00001	0.6	2	31	5.5	Y	Y	100	100	10	Y	16 Ld QSOP
ISL28272		0.1	0.5	2.4	5	0.078	78	0.00001	0.5	2	31	5.5	Y	Y	100	100	100	Y	15 Ld QSOP T+R, 16 Ld QSOP
ISL28195		0.01	0.0042	1.8	5.5	0.001	150	0.000015	2	1	11	4.97	Y	Y	100	100	1	Y	6 Ld SOT-23 T+R
ISL28194		0.0035	0.0012	1.8	5.5	0.00033	265	0.000015	2	1	11	4.97	Y	Y	100	100	1	Y	6 Ld SOT-23 T+R

www.BDTIC.com/Intersil

Bandwidth <10MHz - Buffers - Programmable Gamma Reference Generators

Device	BW (MHz)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	Enable	Package
EL5525	5	4.5	16.5	15	18	140	14.9	N	Y	60	Y	38 Ld HTSSOP
EL5825	5	4.5	16.5	8	8	140	14.9	N	Y	60	Y	24 Ld QFN, 24 Ld TSSOP
EL5126	0.4	4.5	16.5	10	8	240	14.9	N	Y	60		32 Ld QFN
EL5226	0.4	5	16.5	9	10	240	14.9	N	Y	65	N	28 Ld TSSOP
EL5326	0.4	5	16.5	10	12	240	14.9	N	Y	65	N	28 Ld TSSOP

Bandwidth <10MHz - Buffers - Gamma Buffers

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	Gain A_V (min)	Enable	Package
EL5027	2.5	1.2	4.5	16.5	0.11	11	0.05	15	2	120	9.84	Y	Y	80	1	N	6 Ld TSOT
EL5127	2.5	2.2	4.5	16.5	13.3	12	0.002	1	4	120	14.9	Y	Y	80	1	N	10 Ld MSOP
EL5227	2.5	2.2	4.5	16.5	0.33	12	0.002	1	8	120	14.9	Y	Y	80	1	N	20 Ld TSSOP, 24 Ld QFN
EL5327	2.5	2.2	4.5	16.5	0.133	12	0.002	1	10	120	14.9	Y	Y	80	1	N	24 Ld QFN, 24 Ld TSSOP
EL5427	2.5	2.2	4.5	16.5	0.133	12	0.002	1	12	120	14.9	Y	Y	80	1	N	28 Ld TSSOP, 32 Ld QFN

 Copyright © 2008 Intersil Americas Inc. All rights reserved.

By Input Offset Voltage

V_{OS} <25µV - Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ICL7650S		2	2.5	4.5	16	2		0.004	0.0007	1	4.5	9.9	N	N	140	140	1	N	14 Ld PDIP, 8 Ld PDIP, 8 Ld SOIC
HA-5127	Y	8.5	10	10	30	3.5	3	0.015	0.01	1	25	27	N	N	96	120	1	N	8 Ld SOIC
HA-5127A	Y	8.5	10	10	30	3.5	3	0.01	0.01	1	25	27	N	N	114	126	1	N	8 Ld CerDIP
HA-5137A	Y	63	20	10	40	3.5	3	0.01	0.01	1	3.5	27.6	N	N	114	126	5	N	8 Ld CerDIP
ISL28136		5	1.9	2.4	5.5	0.9	15	0.016	0.01	1	56	4.99	Y	Y	99	114	1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
ISL28470		0.24	0.5	2.4	5.5	0.065	60	0.00025	0.015	4	29	4.99	Y	Y	110	110	100	Y	28 Ld QSOP

V_{OS} ≤100µV - Amplifiers

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
HA-5147	120	35	10	40	3.5	3.2	0.015	0.03	1	25	27	N	N	96	120	10	N	8 Ld CerDIP
EL8176	0.4	0.13	2.4	5.5	0.055	28	0.0005	0.1	1	31	4.994	Y	Y	110	110	1	Y	6 Ld SOT-23 T+R, 6 Ld WLCSP T+R, 8 Ld SOIC
ISL28276	0.4	0.2	2.4	5	0.06	30	0.00005	0.1	2	30	4.99	Y	Y	115	115	1	Y	16 Ld QSOP, 8 Ld SOIC
ISL28476	0.4	0.2	2.4	5	0.06	30	0.0005	0.1	4	30	4.99	Y	Y	115	115	1	N	16 Ld QSOP

www.BDTIC.com/Intersil

V_{OS} ≤500µV - Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ISL28270		0.24	0.5	2.4	5.5	0.06	60	0.0005	0.15	2	29	4.99	Y	Y	110	110	100	Y	16 Ld QSOP
ISL28278		0.3	0.14	2.4	5	0.06	45	0.00001	0.225	2	26	4.99	Y	Y	105	100	1	Y	16 Ld QSOP
ISL28478		0.3	0.14	2.4	5	0.06	45	0.00001	0.225	4	26	4.99	Y	Y	105	100	1	N	16 Ld QSOP
EL8170		0.192	0.5	2.9	5	0.078	50	0.0007	0.25	1	29	5.5	Y	Y	104	108	100	Y	8 Ld SOIC
EL8172		0.172	0.5	2.9	5	0.078	70	0.00001	0.3	1	29	5.5	Y	Y	104	108	100	Y	8 Ld SOIC
ISL28138		4.5	4	2.4	5.5	0.9	26	0.000001	0.3	1	75	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23, 8 Ld SOIC
ISL28146		5	1.9	2.4	5.5	0.95	12	0.016	0.4	1	56	4.99	Y	Y	99	114	1	Y	6 Ld SOT-23 T+R
ISL28156		0.25	0.05	2.4	5.5	0.039	46	0.005	0.4	1	31	4.99	Y	Y	104	110	1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
HA-2600	Y	12	7	10	40	3	11	0.001	0.5	1	22	24	N	N	90	100	1	N	8 Ld Can, 8 Ld CerDIP
HA-2620	Y	100	35	10	40	3	11	0.001	0.5	1	22	24	N	N	90	100	5	N	8 Ld Can
HA-5102	Y	8	3	5	18	3	4.3	130	0.5	2	15	26	N	N	100	95	1	N	8 Ld Can, 8 Ld CerDIP
HA-5104	Y	8	3	5	18	5	4.3	130	0.5	4	15	26	N	N	100	95	1	N	14 Ld CerDIP, 16 Ld SOIC, 20 Ld LCC
ISL28272		0.1	0.5	2.4	5	0.078	78	0.00001	0.5	2	31	5.5	Y	Y	100	100	100	Y	15 Ld QSOP T+R, 16 Ld QSOP

 Copyright © 2008 Intersil Americas Inc. All rights reserved.

V_{OS} ≤500µV - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ISL55190		800	268	3	5	16	1.2	25	0.5	1	130	4.97	N	Y	100	95	5	Y	8 Ld DFN, 8 Ld SOIC
ISL55290		800	268	3	5	16	1.2	25	0.5	2	130	4.97	N	Y	100	95	5	Y	10 Ld MSOP

V_{OS} <1mV - Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ISL28166		0.25	0.05	2.4	5	0.039	46	0.005	0.6	1	31	4.98	Y	Y	104	110	1	Y	6 Ld SOT-23 T+R
ISL28271		0.18	0.5	2.4	5	0.078	240	0.00001	0.6	2	31	5.5	Y	Y	100	100	10	Y	16 Ld QSOP
ISL28273		0.23	0.6	2.4	5.5	0.06	210	0.0002	0.6	2	29	4.99	Y	Y	95	110	10	Y	16 Ld QSOP
ISL28286		0.4	0.17	2.4	5	0.06	50	0.0005	0.6	2	30	4.99	Y	Y	115	115	1	Y	10 Ld MSOP
ISL28486		0.4	0.17	2.4	5	0.06	50	0.0005	0.6	4	26	4.99	Y	Y	115	115	1	N	16 Ld QSOP
ISL28191		61	17	3	5.5	3.5	1.7	6	0.63	1	130	4.97	N	Y	80	100	1	Y	6 Ld SOT-23 T+R, 6 Ld µTDFN T+R
ISL28291		61	17	3	5.5	3.5	1.7	6	0.63	2	130	4.97	N	Y	80	100	1	Y	10 Ld MSOP, 10 Ld µTQFN T+R, 8 Ld SOIC
ISL28158		0.25	0.1	2.4	5.5	0.034	64	0.000001	0.65	1	30	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23, 8 Ld SOIC
ISL28190		170	50	3	5.5	11	1	16	0.7	1	144	4.99	N	Y	80	103	1	Y	6 Ld SOT-23 T+R, 6 Ld µTDFN T+R
ISL28290		170	50	3	5.5	11	1	16	0.7	2	144	4.97	N	Y	80	103	1	Y	10 Ld MSOP, 10 Ld µTQFN T+R, 8 Ld SOIC
ISL55191		800	260	3	5	6.1	1.3	12	0.8	1	132	4.98	N	Y	77	100	10	Y	8 Ld DFN, 8 Ld SOIC
ISL55291		800	260	3	5	6.1	1.3	12	0.8	2	132	4.98	N	Y	77	100	10	Y	10 Ld MSOP
EL5130	Y	500	350	5	12	4	1.8	2.3	0.9	1	100	7.6	N	N	90	110	5	Y	8 Ld SOIC
EL5131	Y	500	350	5	12	4	1.8	2.3	0.9	1	100	7.6	N	N	90	110	5	N	5 Ld SOT-23

www.BDTIC.com/Intersil

V_{OS} =1mV - Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5127		2.5	2.2	4.5	16.5	13.3	12	0.002	1	4	120	14.9	Y	Y	80		1	N	10 Ld MSOP
EL5132	Y	670	1000	5	12	12	0.9	12	1	1	140	7	N	N	87	100	10	Y	8 Ld SOIC
EL5133	Y	670	1000	5	12	12	0.9	12	1	1	140	7	N	N	87	100	10	N	5 Ld SOT-23
EL5134	Y	650	450	5	12	6.7	1.5	3.7	1	1	140	7.8	N	N	85	108	5	Y	8 Ld SOIC
EL5135	Y	650	450	5	12	6.7	1.5	3.7	1	1	140	7.8	N	N	85	108	5	N	5 Ld SOT-23
EL5150	Y	200	67	5	12	1.4	12	0.02	1	1	70	5.6	N	N	110	100	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5151	Y	200	67	5	12	1.4	12	0.02	1	1	70	5.6	N	N	110	100	1	N	5 Ld SOT-23
EL5152	Y	270	180	5	12	3	12	0.12	1	1	105	7.4	N	N	116	110	1	Y	8 Ld SOIC
EL5153	Y	270	180	5	12	3	12	0.12	1	1	105	7.4	N	N	116	110	1	N	5 Ld SOT-23
EL5156	Y	600	700	5	12	6	12	0.4	1	1	140	7.6	N	N	90	108	1	Y	8 Ld SOIC
EL5157	Y	600	700	5	12	6	12	0.4	1	1	140	7.6	N	N	90	108	1	N	5 Ld SOT-23

Copyright © 2008 Intersil Americas Inc. All rights reserved.

V_{OS} =1mV - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5227		2.5	2.2	4.5	16.5	0.33	12	0.002	1	8	120	14.9	Y	Y	80		1	N	20 Ld TSSOP, 24 Ld QFN
EL5234	Y	650	450	5	12	6.7	1.5	3.7	1	2	140	7.8	N	N	85	108	5	Y	10 Ld MSOP
EL5235	Y	650	450	5	12	6.7	1.5	3.7	1	2	140	7.8	N	N	85	108	5	N	8 Ld SOIC
EL5250	Y	200	67	5	12	1.4	12	0.02	1	2	70	5.6	N	N	110	100	1	Y	10 Ld MSOP
EL5251	Y	200	67	5	12	1.4	12	0.02	1	2	70	5.6	N	N	110	100	1	N	8 Ld MSOP, 8 Ld SOIC
EL5252	Y	270	180	5	12	3	12	0.11	1	2	105	7.4	N	N	95	110	1	Y	10 Ld MSOP
EL5256	Y	600	700	5	12	6	12	0.02	1	2	140	7.6	N	N	90	108	1	Y	10 Ld MSOP
EL5257		600	700	5	12	6	12	0.02	1	2	140	7.6	N	N	90	108	1	N	8 Ld MSOP, 8 Ld SOIC
EL5327		2.5	2.2	4.5	16.5	0.133	12	0.002	1	10	120	14.9	Y	Y	80		1	N	24 Ld QFN, 24 Ld TSSOP
EL5427		2.5	2.2	4.5	16.5	0.133	12	0.002	1	12	120	14.9	Y	Y	80		1	N	28 Ld TSSOP, 32 Ld QFN
EL5451	Y	200	67	5	12	1.4	12	0.02	1	4	70	5.6	N	N	110	100	1	N	14 Ld SOIC
EL5455	Y	270	180	5	12	3	12	0.12	1	4	105	7.4	N	N	96	110	1	N	14 Ld SOIC
EL8171		0.45	0.5	2.9	5	0.078	200	0.00001	1	1	29	5.5	Y	Y	90	104	10	Y	8 Ld SOIC
EL8173		0.396	0.5	2.9	5	0.078	200	0.0007	1	1	29	5.5	Y	Y	90	104	10	Y	8 Ld SOIC
ISL59311		650	1500	4.5	5.5	17	15	6	1	3	60	4.4	N	N	75	75	2	Y	32 Ld QFN

V_{OS} ≤4mV - Amplifiers

www.BDTIC.com/Intersil

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5462		500	4000	5	12	1.5	3	2	1.5	4	100	7.2	N	N	76	62	1	N	14 Ld SOIC
EL8178		0.266	0.15	2.4	5.5	0.055	48	0.000025	1.5	1	31	4.994	Y	Y	100	100	1	Y	6 Ld SOT-23 T+R, 6 Ld WLCSP T+R, 8 Ld SOIC
EL8188		0.266	0.15	2.4	5	0.055	48	0.000075	1.5	1	31	4.994	Y	Y	100	100	1	Y	6 Ld SOT-23, 8 Ld SOIC
ISL28288		0.3	0.14	2.4	5	0.06	48	0.00001	1.5	2	31	4.99	Y	Y	105	100	1	Y	10 Ld MSOP, 8 Ld SOIC
ISL28488		0.3	0.14	2.4	5	0.06	48	0.00001	1.5	4	31	4.99	Y	Y	105	100	1	N	14 Ld TSSOP, 16 Ld QSOP
ISL28168		0.2	0.1	2.4	5.5	0.034	64	0.00001	1.6	1	30	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23
ISL28148		4.5	4	2.4	5.5	1.1	28	0.000001	1.8	1	75	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23 T+R, 6 Ld WLCSP T+R
CA3130A		15	30	5	16	2	23000	0.000005	2	1	22	13.3	N	N	90	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
CA3140A		4.5	9	4	36	4	12	0.00001	2	1	40	27.4	N	N	80	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
CA3240A		4.5	9	4	36	4	12	0.00001	2	2	40	27.4	N	N	80	90	1	N	8 Ld PDIP
CA3260A		4	10	4	16	1.2	25	0.000002	2	2	22	15	N	N	90	95	1	N	8 Ld PDIP
CA5260A		3	5	4.5	16	1.8	25	0.000002	2	2	22	15	N	Y	84	85	1	N	8 Ld SOIC
CA5420A		0.5	0.5	2	20	0.45	38	0.000002	2	1	2.7	19.75	Y	N	83	83	1	Strobe	8 Ld SOIC
EL2125	Y	175	185	5	30	10	0.83	22	2	1	100	27.1	N	N	97	106	10	N	5 Ld SOT-23, 8 Ld SOIC

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

V_{OS} ≤4mV - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL2126	Y	90	110	5	30	4.5	1.3	7	2	1	100	27.5	N	N	100	106	10	N	5 Ld SOT-23, 8 Ld SOIC
ISL28194		0.0035	0.0012	1.8	5.5	0.00033	265	0.000015	2	1	11	4.97	Y	Y	100	100	1	Y	6 Ld SOT-23 T+R
ISL28195		0.01	0.0042	1.8	5.5	0.001	150	0.000015	2	1	11	4.97	Y	Y	100	100	1	Y	6 Ld SOT-23 T+R
EL5176		250	800	4.75	11	7.5	26	6	2.5	1	40	7.8	N	N	84	82	1	Y	10 Ld MSOP
EL1516		350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	N	8 Ld MSOP, 8 Ld SOIC
EL1516A		350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	Y	10 Ld MSOP
EL2227	Y	115	50	5	24	4.8	1.9	3.4	3	2	100	20.8	N	N	95	94	2	N	8 Ld MSOP, 8 Ld SOIC
EL2228		80	65	5	24	4.5	4.9	4.5	3	2	180	20.6	N	N	83	90	1	N	8 Ld MSOP, 8 Ld SOIC
HA-2625	Y	100	35	10	40	3	11	0.005	3	1	18	24	N	N	90	100	5	N	8 Ld PDIP, 8 Ld SOIC
HA5023	Y	125	475	9	30	7.5	4.5	4	3	2	20	6	N	N	60	53	1	N	8 Ld PDIP, 8 Ld SOIC
HA5024	Y	125	475	9	30	7.5	4.5	4	3	4	20	6	N	N	60	53	1	Y	20 Ld PDIP, 20 Ld SOIC
HA-5160		100	120	14	36	8	35	0.00002	3	1	20	22	N	N	86	80	1	N	8 Ld Can
EL5100	Y	200	2200	5	10	2.5	10	2	4	1	100	6.8	N	N	90	75	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5101	Y	200	2200	5	10	2.5	10	2	4	1	100	6.8	N	N	90	75	1	N	5 Ld SOT-23
EL5300	Y	200	2200	5	10	2.5	10	2	4	3	100	6.8	N	N	90	75	1	Y	16 Ld QSOP
HA-2404		40	30	20	40	4.8	20	0.05	4	4	20	24	N	N	90	100	1	Y	16 Ld CerDIP
HA-2640		4	5	20	80	3.2	15	0.01	4	1	15	70	N	N	90	100	1	N	8 Ld Can, 8 Ld CerDIP
ICL7611		1.4	1.6	2	16	0.01	100	0.001	4	1	40	9.8	N	N	94	96	1	N	8 Ld PDIP, 8 Ld SOIC
ICL7612		1.4	1.6	2	16	0.01	100	0.001	4	1	40	9.8	N	N	94	96	1	N	8 Ld PDIP, 8 Ld SOIC
ICL7621		0.48	0.16	2	16	0.1	100	0.001	4	2	1	9.8	N	N	86	91	1	N	8 Ld PDIP, 8 Ld SOIC

www.BDTIC.com/Intersil

V_{OS} >4mV - Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
CA3140		4.5	9	4	36	4	12	0.00001	5	1	40	27.4	N	N	80	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
CA3240		4.5	9	4	36	4	12	0.00001	5	2	40	27.4	N	N	80	90	1	N	8 Ld PDIP
CA3420		0.5	0.5	2	20	0.45	38	0.000005	5	1	1.5	19.75	N	Y	80	65	1	Strobe	8 Ld PDIP
EL5102	Y	400	2200	5	10	5.2	12	2	5	1	150	7.8	N	N	80	80	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5103	Y	400	2200	5	10	5.2	6	2	5	1	150	7.8	N	N	80	80	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5108		450	4500	5	12	3.5	2	2	5	1	135	7.6	N	N	75		1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5160	Y	200	1700	5	10	0.75	4	5	5	1	70	6.8	N	N	74	62	1	Y	6 Ld SOT-23, 8 Ld SOIC

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

V_{OS} >4mV - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5161	Y	200	1700	5	10	0.75	4	5	5	1	70	6.8	N	N	74	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5162	Y	500	4000	5	12	1.5	3	2	5	1	100	7.2	N	N	76	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5163	Y	500	4000	5	12	1.5	3	2	5	1	100	7.2	N	N	76	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5164	Y	600	4700	5	12	5	2.1	2	5	1	140	7.6	N	N	79	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5165	Y	600	4700	5	12	5	2.1	2	5	1	140	7.6	N	N	79	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5166	Y	1400	6000	5	12	8.5	1.7	8.5	5	1	160	7.6	N	N	50	57	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5167	Y	1400	6000	5	12	8.5	1.7	8.5	5	1	160	7.6	N	N	50	57	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5202	Y	400	2200	5	10	5.2	12	2	5	2	150	7.8	N	N	80	80	1	Y	10 Ld MSOP
EL5203	Y	400	2200	5	10	5.2	12	2	5	2	150	7.8	N	N	80	80	1	N	8 Ld MSOP, 8 Ld SOIC
EL5260	Y	200	1700	5	10	0.75	4	5	5	2	70	6.8	N	N	74	62	1	Y	10 Ld MSOP
EL5261	Y	200	1700	5	10	0.75	4	5	5	2	70	6.8	N	N	74	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5262	Y	500	4000	5	12	1.5	3	2	5	2	100	7.2	N	N	76	62	1	Y	10 Ld MSOP
EL5263	Y	500	4000	5	12	1.5	3	2	5	2	100	7.2	N	N	76	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5302	Y	400	2200	5	10	5.2	12	2	5	3	150	7.8	N	N	80	80	1	Y	16 Ld QSOP
EL5308	Y	450	4500	5	12	3.5	2	2	5	3	135	7.6	N	N	75	1	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5360	Y	200	1700	5	10	0.75	4	5	5	3	70	6.8	N	N	74	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5362	Y	500	4000	5	12	1.5	3	2	5	3	100	7.2	N	N	76	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5364	Y	600	4700	5	12	5	2.1	2	5	3	140	7.6	N	N	79	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5367		1000	6000	5	12	8.5	1.7	8.5	5	3	160	7.6	N	N	50	57	1	N	16 Ld QSOP
EL8300	Y	200	200	3	5.5	2	10	1.4	5	3	65	4.83	N	Y	100	90	1	Y	16 Ld QSOP, 16 Ld SOIC
HFA1105		330	1000	9	11	5.8	3.5	2	5	1	60	6.8	N	N	54	50	1	N	8 Ld SOIC
HFA1109	Y	450	1100	9	11	10	4	4	5	1	36	6.4	N	N	53	50	1	N	8 Ld SOIC
HFA1135		360	1200	9	11	6.9	3.5	6	5	1	60	6.8	N	N	54	50	1	N	8 Ld SOIC
HFA1305		560	2500	9	11	5.8	3.5	6	5	3	60	6.6	N	N	52	48	1	N	14 Ld SOIC
HFA1405		560	2500	9	11	5.8	3.5	6	5	4	60	6.6	N	N	52	48	1	N	14 Ld PDIP, 14 Ld SOIC
ISL55001		220	300	5	30	9	12	0.6	5	1	140	26.15	N	N	100	90	1	N	8 Ld SOIC
ISL55002		200	300	5	30	8.5	12	0.6	5	2	140	26.15	N	N	100	90	1	N	8 Ld SOIC
ISL55004		200	300	5	30	8.5	12	0.6	5	4	140	26.15	N	N	100	90	1	N	14 Ld SOIC
CA3260		4	10	4	16	1.2	25	0.000002	6	2	22	15	N	N	90	90	1	N	8 Ld PDIP
CA5260		3	5	4.5	16	1.8	25	0.000002	6	2	22	15	N	Y	84	85	1	N	8 Ld SOIC
EL8100	Y	200	200	3	5	2	10	1.5	6	1	65	4.8	N	Y	100	90	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL8101	Y	200	200	3	5	2	10	1.5	6	1	65	4.8	N	Y	100	90	1	N	5 Ld SOT-23

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

V_{OS} >4mV - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL8200	Y	200	200	3	5.5	2	10	1.6	6	2	65	4.8	N	Y	100	90	1	Y	10 Ld MSOP
EL8201	Y	200	200	3	5.5	2	10	1.6	6	2	65	4.8	N	Y	100	90	1	N	8 Ld SOIC
EL8401	Y	200	200	3	5.5	2	10	1.6	6	4	65	4.8	N	Y	100	90	1	N	14 Ld SOIC, 16 Ld QSOP
HA-2645		4	5	20	80	3.2	15	0.012	6	1	12	70	N	N	90	100	1	N	8 Ld Can, 8 Ld CerDIP
HFA1100	Y	850	2300	4.5	11	21	4	2.5	6	1	60	6.6	N	N	50	46	1	N	8 Ld PDIP, 8 Ld SOIC
HFA1130		850	2300	9	11	21	4	25	6	1	60	6.6	N	N	50	46	1	N	20 Ld LCC, 8 Ld SOIC
HFA1145		330	1000	9	11	5.9	3.5	6	6	1	60	6.8	N	N	54	50	1	Y	8 Ld PDIP, 8 Ld SOIC
HFA1155		360	1650	4.5	11	5.5	4.7	25	6	1	55	6.6	N	N	50	46	1	N	5 Ld SOT-23 T+R
EL2045	Y	100	275	4	36	5.2	15	2.8	7	1	75	27.2	N	N	85	95	2	N	8 Ld PDIP, 8 Ld SOIC
EL8302	Y	500	600	3	5.5	5.6	12	6	7	3	65	4.85	N	Y	95	95	1	Y	16 Ld QSOP, 16 Ld SOIC
ISL1557		300	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1557A		300	1200	4.5	13.2	15	8.2	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1571		250	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95		1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1572		280	1200	4.5	13.2	15		3	7.5	2	1000	10	N	N	95		1	Y	10 Ld HMSOP, 16 Ld QFN
CA3130		15	30	5	16	2	23000	0.000005	8	1	22	13.3	N	N	90	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
EL8102	Y	500	600	3	5	5.6	12	6	8	1	65	4.8	N	Y	95	95	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL8103	Y	500	600	3	5	5.6	12	6	8	1	65	4.8	N	Y	95	95	1	N	5 Ld SOT-23
EL8202	Y	500	600	3	5.5	5.6	12	6	8	2	65	4.8	N	Y	95	95	1	Y	10 Ld MSOP
EL8203	Y	500	600	3	5.5	5.6	12	6	8	2	65	4.8	N	Y	95	95	1	N	8 Ld MSOP, 8 Ld SOIC
EL8403	Y	500	600	3	5.5	5.6	12	6	8	4	65	4.8	N	Y	95	85	1	N	14 Ld SOIC, 16 Ld QSOP
HA-2510/883		12	50	20	40	6		0.2	8	1	65	20	N	N	80	80	1	N	8 Ld Can, 8 Ld CerDIP
HA-2520	Y	20	120	16	36	4	20	0.1	8	1	10	24	N	N	90	90	3	N	8 Ld Can, 8 Ld CerDIP
HA-2540	Y	400	400	20	30	20	6	5	8	1	20	20	N	N	70	72	10	N	14 Ld CerDIP
HA-5020	Y	100	800	10	30	10	4.5	3	8	1	20	25.4	N	N	64	60	1	Y	20 Ld LCC, 8 Ld PDIP, 8 Ld SOIC
EL1508		80	600	5	12	3.5	3.5	50	10	2	1000	21.6	N	N			1	Y	16 Ld SOIC, 20 Ld SOIC, 24 Ld QFN
EL1526		46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	20 Ld HTSSOP, 24 Ld QFN
EL1528		40	210	10	24	5	3.5	30	10	2	1000	22.2	N	N	75		1	Y	20 Ld HTSSOP, 24 Ld QFN
EL1529		46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	24 Ld QFN

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

V_{OS} >4mV - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL1537		50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537A		50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537B		50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL2142		150	400	6	12.6	11	36	6	10	1	60	6.2	N	N	70	70	1	N	8 Ld SOIC
EL2480	Y	250	1200	3	12	3	3	15	10	4	55	8	N	N	70	50	1	N	14 Ld SOIC
EL4543		350	1200	5	12	14.5	27	15	10	3	60		N	N	80	80	2	Y	Eval Board, 20 Ld QFN, 24 Ld QSOP
EL5104	Y	700	7000	5	10	9.5	10	8	10	1	160	7.6	N	N	70	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5105	Y	700	7000	5	10	9.5	10	8	10	1	160	7.6	N	N	70	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5106	Y	350	4500	5	12	1.5	2.8	1.5	10	1	100	7.2	N	N	75		1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5192	Y	600	2800	5	10	6	4.1	3	10	1	120	7.4	N	N	75	50	1	Y	5 Ld SOT-23
EL5192A		600	2800	5	10	6	4.1	3	10	1	120	7.4	N	N	75	50	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5193	Y	300	2600	5	10	4	4.4	1	10	1	120	7.4	N	N	75	50	1	Y	5 Ld SOT-23 T+R, 6 Ld SOT-23 T+R
EL5193A		300	2600	5	10	4	4.4	1	10	1	120	7.4	N	N	75	50	1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
EL5197	Y	200	2200	5	10	4	4.4	1	10	1	120	7.4	N	N	75		1	Y	5 Ld SOT-23
EL5197A		200	2200	5	10	4	4.4	1	10	1	120	7.4	N	N	75		1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
EL5204	Y	700	7000	5	10	9.5	10	8	10	2	160	7.6	N	N	70	62	1	Y	10 Ld MSOP
EL5205	Y	700	7000	5	10	9.5	10	8	10	2	160	7.6	N	N	70	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5292	Y	600	2300	5	10	6	4.1	4	10	2	120	7.4	N	N	75	50	1	Y	8 Ld SOIC
EL5293	Y	300	2200	5	10	4	4.4	1	10	2	120	7.4	N	N	75	50	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5304	Y	700	7000	5	10	9.5	10	8	10	3	160	7.6	N	N	70	62	1	Y	16 Ld QSOP
EL5306	Y	350	4500	5	12	1.5	2.8	1.5	10	3	100	7.2	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
EL5392A	Y	600	2300	5	10	6	4.1	4	10	3	120	7.4	N	N	75	50	1	Y	16 Ld QSOP
EL5393A	Y	300	2200	5	10	4	4.4	1	10	3	120	7.4	N	N	75	50	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5397A		200	2100	5	10	4	4.8	1	10	3	120	7.4	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
HA-2522	Y	20	120	16	36	4	20	0.125	10	1	10	24	N	N	90	90	3	N	8 Ld Can
HA-2525	Y	20	120	16	36	4	20	0.125	10	1	10	24	N	N	90	90	3	N	8 Ld Can, 8 Ld CerDIP, 8 Ld PDIP, 8 Ld SOIC
HFA1412		350	1650	9	11	5.9	7	1	10	4	55	6.4	N	N	49	45	1	N	14 Ld PDIP, 14 Ld SOIC
ISL55036		400	2500	3	5.5	7.2	12.5	5.5	10	6	95	4.86	N	Y	78		4	Y	24 Ld TQFN
EL2250		125	300	2.7	12	5	48	5.5	12	2	100	10.8	N	N	70	70	1	N	8 Ld SOIC
EL5123		12	15	4.5	16.5	0.5	9.5	0.002	12	4	120	14.9	Y	Y	80		1	N	10 Ld MSOP
EL5223		12	15	4.5	16.5	0.5	9.5	0.002	12	8	120	14.9	Y	Y	80			N	20 Ld TSSOP, 24 Ld QFN

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

V_{OS} >4mV - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5323		12	15	4.5	16.5	0.5	9.5	0.002	12	10	120	14.9	Y	Y	80		1	N	24 Ld QFN, 24 Ld TSSOP
EL5423		12	15	4.5	16.5	0.5	9.5	0.002	12	12	120	14.9	Y	Y	80		1	N	28 Ld TSSOP, 32 Ld QFN
EL5421		12	10	4.5	16.5	0.5	10	0.002	14	4	120	14.9	Y	Y	80		1	N	10 Ld MSOP, 10 Ld MSOP
EL2276		70	800	3	12.6	1	5.1	15	15	2	55	8	N	N	70	50	1	Y	14 Ld SOIC
EL5027		2.5	1.2	4.5	16.5	0.11	11	0.05	15	2	120	9.84	Y	Y	80		1	N	6 Ld TSOT
EL5191		1000	2800	5	10	9	3.8	40	15	1	120	7.4	N	N	75	50	1	Y	5 Ld SOT-23 T+R, 8 Ld SOIC
EL5191A		1000	2800	5	10	9	3.8	40	15	1	120	7.4	N	N	75	50	1	Y	8 Ld SOIC
EL5396A		400	2600	5	10	9	3.8	40	15	3	120	7.4	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
EL5444		100	200	4.75	5.25	7	25	2	15	4	90	4.85	N	Y	60	50	1	N	14 Ld PDIP, 14 Ld SOIC, 16 Ld QSOP
HA-2539	Y	600	600	20	30	20	6	5	15	1	20	20	N	N	70	72	10	N	14 Ld PDIP
EL1506		70	500	5	24	3.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	24 Ld SOIC, 28 Ld HTSSOP
EL1507		70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	16 Ld QFN, 16 Ld SOIC
EL1510		70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	62		1	N	8 Ld DFN, 8 Ld SOIC
EL1527		70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	60		1	Y	28 Ld HTSSOP
EL1509		70	500	5	12	7	2.8	30	20	2	450	10	N	N	50		1	N	8 Ld DFN, 8 Ld SOIC
EL1511		70	500	5	15	3.5	2.8	30	20	2	450	13	N	N	50		1	Y	16 Ld QFN, 16 Ld SOIC
EL1519		70	350	5	12	7.5	2.7	30	20	2	450	10	N	N	50		1	N	8 Ld SOIC
EL5623		10	9	4.5	18	0.65	16	0.002	20	6	120	14.9	Y	Y	80		1	N	16 Ld TSSOP
EL1517B		200	800	5	12	7	6	5	25	2	250	10	N	N	90		1	Y	10 Ld HMSOP, 16 Ld QFN, 8 Ld SOIC
EL5144		100	200	4.75	5.25	7	25	2	25	1	90	4.85	N	Y	60	50	1	N	5 Ld SOT-23
EL5146		100	200	4.75	5.25	7	25	2	25	1	90	4.85	N	Y	60	50	1	Y	8 Ld PDIP, 8 Ld SOIC
EL5170		100	1100	5	10	7.4	30	6	25	1	85	6.9	N	N	83	84	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5171		250	800	5	10	8	26	6	25	1	90	6.8	N	N	84	82	1	N	8 Ld SOIC
EL5172		250	800	5	10	6	26	6	25	1	95	7.5	N	N	58	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5174		550	1100	4.75	11	12.5	21	14	25	1	60	7.6	N	N	75	78	1	N	8 Ld SOIC
EL5177		550	1100	4.75	11	12.5	21	14	25	1	50	7.6	N	N	75	78	1	Y	10 Ld MSOP
EL5244		100	200	4.75	5.25	7	25	2	25	2	90	4.85	N	Y	60	50	1	N	8 Ld MSOP, 8 Ld PDIP, 8 Ld SOIC
EL5246		100	200	4.75	5.25	7	25	2	25	2	90	4.85	N	Y	60		1	Y	10 Ld MSOP, 14 Ld PDIP, 14 Ld SOIC
EL5370		100	1100	5	10	7	30	6	25	3	85	6.9	N	N	83	84	2	Y	24 Ld QSOP

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

V_{OS} >4mV - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5371		250	800	5	10	8	26	6	25	3	90	6.8	N	N	84	82	1	N	28 Ld QSOP, 28 Ld QSOP
EL5374		550	1100	4.75	11	12.5	21	14	25	3	60	7.6	N	N	75	78	1	Y	28 Ld QSOP
EL8108		300	1100	4.5	13	14.3	6	5	25	2	450	10	N	N	90		1	Y	16 Ld QFN, 8 Ld SOIC
HFA1110		750	1300	9	11	26	14	10	25	1	60	6.6	N	N	46	60	1	N	8 Ld SOIC
HFA1112		850	2400	9	11	21	9	25	25	1	60	6.6	N	N	45		1	N	8 Ld PDIP, 8 Ld SOIC
HFA1113		850	2400	9	11	21	9	25	25	1	60	6.6	N	N	45		1	N	8 Ld SOIC
ISL59830		200	500	3	3.6	40	20		25	3	50	5.1	Y	Y	90		2	Y	16 Ld QSOP
ISL59830A		200	500	3	3.6	40	20		25	3	50	5.1	Y	Y	90		2	Y	16 Ld QSOP
ISL59833		200	500	3	3.6	32.3	20		25	3	50	5.1	Y	Y	68		2	Y	16 Ld QSOP
ISL59837		200	500	3	3.6	32.3	20		25	3	50	5.1	Y	Y	62		2	Y	16 Ld QSOP
EL1503A		80	1100	10	24	12.5	3.5	50	30	2	1000	21.2	Y	N	90		1	Y	20 Ld SOIC, 24 Ld QFN
EL5173		450	900	5	11	12	25	11	30	1	55	7.7	N	N	73	80	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5178		700	850	4.75	11	12.5	18	14	30	1	100	7.4	N	N	75	78	2	N	8 Ld MSOP, 8 Ld SOIC
EL5373		450	1100	4.75	11	12	25	13	30	3	55	7.7	N	N	73	80	2	Y	24 Ld QSOP
EL5375		550	900	4.75	11	9.6	21	12.5	30	3	67	7.49	N	N	56	95	1	Y	24 Ld QSOP
EL5378		700	1000	4.75	11	12.5	18	14	30	3	60	7.4	N	N	75	78	2	Y	28 Ld QSOP
ISL55020		300	1200	4.5	12	21	6.9	7	8	1	150	10	N	N	60		1	Y	16 Ld QFN T+R
EL2141		150	800	6	12.6	11	36	6	40	1	60	7.2	N	N	70		2	N	8 Ld SOIC
EL5175	Y	550	600	4.75	11	9.6	21	12.5	40	1	67	7.49	N	N	56	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL9110		150	1500	10	12	33		1	250	1	60	7	N	N	60	60	1	Y	16 Ld QSOP

www.BDTIC.com/Intersil

V_{OS} >1mV - Buffers - General

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	Gain A_V (min)	Enable	Package
HA-5002	Y	110	1300	10	40	8.3		2	5	1	200	27	N	N	64	0.995	N	20 Ld PLCC, 8 Ld Can, 8 Ld PDIP, 8 Ld SOIC
HA-5033	Y	250	1100	10	32	21	20	20	5	1	100	22	N	N	54		N	12 Ld Can, 8 Ld PDIP
HA4600		480	1700	9	11	10.5		30	10	1	20	5.6	N	N		1	Y	6 Ld SOT-23, 8 Ld PDIP, 8 Ld SOIC, 8 Ld SOT-23 T+R

By Supply Current

I_S ≤10µA - Amplifiers

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ISL28194	0.0035	0.0012	1.8	5.5	0.00033	265	0.000015	2	1	11	4.97	Y	Y	100	100	1	Y	6 Ld SOT-23 T+R
ISL28195	0.01	0.0042	1.8	5.5	0.001	150	0.000015	2	1	11	4.97	Y	Y	100	100	1	Y	6 Ld SOT-23 T+R

 Copyright © 2008 Intersil Americas Inc. All rights reserved.

I_S ≤10µA - Amplifiers (Continued)

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail		PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
												Input	Output					
ICL7611	1.4	1.6	2	16	0.01	100	0.001	4	1	40	9.8	N	N	94	96	1	N	8 Ld PDIP, 8 Ld SOIC
ICL7612	1.4	1.6	2	16	0.01	100	0.001	4	1	40	9.8	N	N	94	96	1	N	8 Ld PDIP, 8 Ld SOIC

I_S ≤100µA - Amplifiers

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail		PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
												Input	Output					
ISL28158	0.25	0.1	2.4	5.5	0.034	64	0.000001	0.65	1	30	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23, 8 Ld SOIC
ISL28168	0.2	0.1	2.4	5.5	0.034	64	0.00001	1.6	1	30	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23
ISL28156	0.25	0.05	2.4	5.5	0.039	46	0.005	0.4	1	31	4.99	Y	Y	104	110	1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
ISL28166	0.25	0.05	2.4	5	0.039	46	0.005	0.6	1	31	4.98	Y	Y	104	110	1	Y	6 Ld SOT-23 T+R
EL8176	0.4	0.13	2.4	5.5	0.055	28	0.0005	0.1	1	31	4.994	Y	Y	110	110	1	Y	6 Ld SOT-23 T+R, 6 Ld WLCSP T+R, 8 Ld SOIC
EL8178	0.266	0.15	2.4	5.5	0.055	48	0.000025	1.5	1	31	4.994	Y	Y	100	100	1	Y	6 Ld SOT-23 T+R, 6 Ld WLCSP T+R, 8 Ld SOIC
EL8188	0.266	0.15	2.4	5	0.055	48	0.000075	1.5	1	31	4.994	Y	Y	100	100	1	Y	6 Ld SOT-23, 8 Ld SOIC
ISL28270	0.24	0.5	2.4	5.5	0.06	60	0.0005	0.15	2	29	4.99	Y	Y	110	110	100	Y	16 Ld QSOP
ISL28273	0.23	0.6	2.4	5.5	0.06	210	0.0002	0.6	2	29	4.99	Y	Y	95	110	10	Y	16 Ld QSOP
ISL28276	0.4	0.2	2.4	5	0.06	30	0.00005	0.1	2	30	4.99	Y	Y	115	115	1	Y	16 Ld QSOP, 8 Ld SOIC
ISL28278	0.3	0.14	2.4	5	0.06	45	0.00001	0.225	2	26	4.99	Y	Y	105	100	1	Y	16 Ld QSOP
ISL28286	0.4	0.17	2.4	5	0.06	50	0.0005	0.6	2	30	4.99	Y	Y	115	115	1	Y	10 Ld MSOP
ISL28288	0.3	0.14	2.4	5	0.06	48	0.00001	1.5	2	31	4.99	Y	Y	105	100	1	Y	10 Ld MSOP, 8 Ld SOIC
ISL28476	0.4	0.2	2.4	5	0.06	30	0.0005	0.1	4	30	4.99	Y	Y	115	115	1	N	16 Ld QSOP
ISL28478	0.3	0.14	2.4	5	0.06	45	0.00001	0.225	4	26	4.99	Y	Y	105	100	1	N	16 Ld QSOP
ISL28486	0.4	0.17	2.4	5	0.06	50	0.0005	0.6	4	26	4.99	Y	Y	115	115	1	N	16 Ld QSOP
ISL28488	0.3	0.14	2.4	5	0.06	48	0.00001	1.5	4	31	4.99	Y	Y	105	100	1	N	14 Ld TSSOP, 16 Ld QSOP
ISL28470	0.24	0.5	2.4	5.5	0.065	60	0.00025	0.015	4	29	4.99	Y	Y	110	110	100	Y	28 Ld QSOP
EL8170	0.192	0.5	2.9	5	0.078	50	0.0007	0.25	1	29	5.5	Y	Y	104	108	100	Y	8 Ld SOIC
EL8171	0.45	0.5	2.9	5	0.078	200	0.00001	1	1	29	5.5	Y	Y	90	104	10	Y	8 Ld SOIC
EL8172	0.172	0.5	2.9	5	0.078	70	0.00001	0.3	1	29	5.5	Y	Y	104	108	100	Y	8 Ld SOIC
EL8173	0.396	0.5	2.9	5	0.078	200	0.0007	1	1	29	5.5	Y	Y	90	104	10	Y	8 Ld SOIC
ISL28271	0.18	0.5	2.4	5	0.078	240	0.00001	0.6	2	31	5.5	Y	Y	100	100	10	Y	16 Ld QSOP
ISL28272	0.1	0.5	2.4	5	0.078	78	0.00001	0.5	2	31	5.5	Y	Y	100	100	100	Y	15 Ld QSOP T+R, 16 Ld QSOP
ICL7621	0.48	0.16	2	16	0.1	100	0.001	4	2	1	9.8	N	N	86	91	1	N	8 Ld PDIP, 8 Ld SOIC

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

I_S ≤1mA - Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
CA3420		0.5	0.5	2	20	0.45	38	0.000005	5	1	1.5	19.75	N	Y	80	65	1	Strobe	8 Ld PDIP
CA5420A		0.5	0.5	2	20	0.45	38	0.000002	2	1	2.7	19.75	Y	N	83	83	1	Strobe	8 Ld SOIC
EL5623		10	9	4.5	18	0.65	16	0.002	20	6	120	14.9	Y	Y	80		1	N	16 Ld TSSOP
EL5160	Y	200	1700	5	10	0.75	4	5	5	1	70	6.8	N	N	74	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5161	Y	200	1700	5	10	0.75	4	5	5	1	70	6.8	N	N	74	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5260	Y	200	1700	5	10	0.75	4	5	5	2	70	6.8	N	N	74	62	1	Y	10 Ld MSOP
EL5261	Y	200	1700	5	10	0.75	4	5	5	2	70	6.8	N	N	74	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5360	Y	200	1700	5	10	0.75	4	5	5	3	70	6.8	N	N	74	62	1	Y	16 Ld QSOP, 16 Ld SOIC
ISL28136		5	1.9	2.4	5.5	0.9	15	0.016	0.01	1	56	4.99	Y	Y	99	114	1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
ISL28138		4.5	4	2.4	5.5	0.9	26	0.000001	0.3	1	75	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23, 8 Ld SOIC, 6 Ld SOT-23, 8 Ld SOIC
EL2276		70	800	3	12.6	1	5.1	15	15	2	55	8	N	N	70	50	1	Y	14 Ld SOIC

I_S ≤1mA - Buffers

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	Gain A_V (min)	Enable	Package
EL5027	2.5	1.2	4.5	16.5	0.11	11	0.05	15	2	120	0.84	Y	Y	80	1	N	6 Ld TSOT
EL5327	2.5	2.2	4.5	16.5	0.133	12	0.002	1	10	120	14.9	Y	Y	80	1	N	24 Ld QFN, 24 Ld TSSOP
EL5427	2.5	2.2	4.5	16.5	0.133	12	0.002	1	12	120	14.9	Y	Y	80	1	N	28 Ld TSSOP, 32 Ld QFN
EL5227	2.5	2.2	4.5	16.5	0.33	12	0.002	1	8	120	14.9	Y	Y	80	1	N	20 Ld TSSOP, 24 Ld QFN
EL5123	12	15	4.5	16.5	0.5	9.5	0.002	12	4	120	14.9	Y	Y	80	1	N	10 Ld MSOP
EL5221	12	10	4.5	16.5	0.5	10	0.002	14	2	120	14.9	Y	Y	80		N	6 Ld SOT-23 T+R, 8 Ld MSOP
EL5223	12	15	4.5	16.5	0.5	9.5	0.002	12	8	120	14.9	Y	Y	80		N	20 Ld TSSOP, 24 Ld QFN
EL5323	12	15	4.5	16.5	0.5	9.5	0.002	12	10	120	14.9	Y	Y	80	1	N	24 Ld QFN, 24 Ld TSSOP
EL5421	12	10	4.5	16.5	0.5	10	0.002	14	4	120	14.9	Y	Y	80	1	N	10 Ld MSOP
EL5423	12	15	4.5	16.5	0.5	9.5	0.002	12	12	120	14.9	Y	Y	80	1	N	28 Ld TSSOP, 32 Ld QFN

www.BDTIC.com/Intersil

I_S >1mA - Amplifiers

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ISL28148		4.5	4	2.4	5.5	1.1	28	0.000001	1.8	1	75	4.99	Y	Y	98	98	1	Y	6 Ld SOT-23 T+R, 6 Ld WLCSP T+R
CA3260		4	10	4	16	1.2	25	0.000002	6	2	22	15	N	N	90	90	1	N	8 Ld PDIP
CA3260A		4	10	4	16	1.2	25	0.000002	2	2	22	15	N	N	90	95	1	N	8 Ld PDIP

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

I_S >1mA - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5150	Y	200	67	5	12	1.4	12	0.02	1	1	70	5.6	N	N	110	100	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5151	Y	200	67	5	12	1.4	12	0.02	1	1	70	5.6	N	N	110	100	1	N	5 Ld SOT-23
EL5250	Y	200	67	5	12	1.4	12	0.02	1	2	70	5.6	N	N	110	100	1	Y	10 Ld MSOP
EL5251	Y	200	67	5	12	1.4	12	0.02	1	2	70	5.6	N	N	110	100	1	N	8 Ld MSOP, 8 Ld SOIC
EL5451	Y	200	67	5	12	1.4	12	0.02	1	4	70	5.6	N	N	110	100	1	N	14 Ld SOIC
EL5106	Y	350	4500	5	12	1.5	2.8	1.5	10	1	100	7.2	N	N	75		1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5162	Y	500	4000	5	12	1.5	3	2	5	1	100	7.2	N	N	76	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5163	Y	500	4000	5	12	1.5	3	2	5	1	100	7.2	N	N	76	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5262	Y	500	4000	5	12	1.5	3	2	5	2	100	7.2	N	N	76	62	1	Y	10 Ld MSOP
EL5263	Y	500	4000	5	12	1.5	3	2	5	2	100	7.2	N	N	76	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5306	Y	350	4500	5	12	1.5	2.8	1.5	10	3	100	7.2	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
EL5362	Y	500	4000	5	12	1.5	3	2	5	3	100	7.2	N	N	76	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5462		500	4000	5	12	1.5	3	2	1.5	4	100	7.2	N	N	76	62	1	N	14 Ld SOIC
CA5260		3	5	4.5	16	1.8	25	0.000002	6	2	22	15	N	Y	84	85	1	N	8 Ld SOIC
CA5260A		3	5	4.5	16	1.8	25	0.000002	2	2	22	15	N	Y	84	85	1	N	8 Ld SOIC
CA3130		15	30	5	16	2	23000	0.000005	8	1	22	13.3	N	N	90	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
CA3130A		15	30	5	16	2	23000	0.000005	2	1	22	13.3	N	N	90	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
EL8100	Y	200	200	3	5	2	10	1.5	6	1	65	4.8	N	Y	100	90	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL8101	Y	200	200	3	5	2	10	1.5	6	1	65	4.8	N	Y	100	90	1	N	5 Ld SOT-23
EL8200	Y	200	200	3	5.5	2	10	1.6	6	2	65	4.8	N	Y	100	90	1	Y	10 Ld MSOP
EL8201	Y	200	200	3	5.5	2	10	1.6	6	2	65	4.8	N	Y	100	90	1	N	8 Ld SOIC
EL8300	Y	200	200	3	5.5	2	10	1.4	5	3	65	4.83	N	Y	100	90	1	Y	16 Ld QSOP, 16 Ld SOIC
EL8401	Y	200	200	3	5.5	2	10	1.6	6	4	65	4.8	N	Y	100	90	1	N	14 Ld SOIC, 16 Ld QSOP
ICL7650S		2	2.5	4.5	16	2		0.004	0.0007	1	4.5	9.9	N	N	140	140	1	N	14 Ld PDIP, 8 Ld PDIP, 8 Ld SOIC
ISL59110		8	40	2.5	3.6	2				1	115	3.2	N	Y	90		2	Y	6 Ld SC-70
ISL59111		8	40	2.5	3.6	2				1	115	3.2	N	Y	90		2	Y	6 Ld CSP
ISL59112		40	85	2.5	3.6	2		5		1	40	3.2	N	N	63		6	N	6 Ld SC-70
EL1526		46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	20 Ld HTSSOP, 24 Ld QFN
EL1529		46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	24 Ld QFN
EL5100	Y	200	2200	5	10	2.5	10	2	4	1	100	6.8	N	N	90	75	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5101	Y	200	2200	5	10	2.5	10	2	4	1	100	6.8	N	N	90	75	1	N	5 Ld SOT-23
EL5300	Y	200	2200	5	10	2.5	10	2	4	3	100	6.8	N	N	90	75	1	Y	16 Ld QSOP
EL2480	Y	250	1200	3	12	3	3	15	10	4	55	8	N	N	70	50	1	N	14 Ld SOIC
EL5152	Y	270	180	5	12	3	12	0.12	1	1	105	7.4	N	N	116	110	1	Y	8 Ld SOIC
EL5153	Y	270	180	5	12	3	12	0.12	1	1	105	7.4	N	N	116	110	1	N	5 Ld SOT-23
EL5252	Y	270	180	5	12	3	12	0.11	1	2	105	7.4	N	N	95	110	1	Y	10 Ld MSOP

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

I_S >1mA - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5455	Y	270	180	5	12	3	12	0.12	1	4	105	7.4	N	N	96	110	1	N	14 Ld SOIC
HA-2600	Y	12	7	10	40	3	11	0.001	0.5	1	22	24	N	N	90	100	1	N	8 Ld Can, 8 Ld CerDIP
HA-2620	Y	100	35	10	40	3	11	0.001	0.5	1	22	24	N	N	90	100	5	N	8 Ld Can
HA-2625	Y	100	35	10	40	3	11	0.005	3	1	18	24	N	N	90	100	5	N	8 Ld PDIP, 8 Ld SOIC
HA-5102	Y	8	3	5	18	3	4.3	130	0.5	2	15	26	N	N	100	95	1	N	8 Ld Can, 8 Ld CerDIP
HA-2640		4	5	20	80	3.2	15	0.01	4	1	15	70	N	N	90	100	1	N	8 Ld Can, 8 Ld CerDIP
HA-2645		4	5	20	80	3.2	15	0.012	6	1	12	70	N	N	90	100	1	N	8 Ld Can, 8 Ld CerDIP
EL1506		70	500	5	24	3.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	24 Ld SOIC, 28 Ld HTSSOP, 24 Ld SOIC, 28 Ld HTSSOP
EL1508		80	600	5	12	3.5	3.5	50	10	2	1000	21.6	N	N			1	Y	16 Ld SOIC, 20 Ld SOIC, 24 Ld QFN
EL1511		70	500	5	15	3.5	2.8	30	20	2	450	13	N	N	50		1	Y	16 Ld QFN, 16 Ld SOIC
EL5108		450	4500	5	12	3.5	2	2	5	1	135	7.6	N	N	75		1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5308	Y	450	4500	5	12	3.5	2	2	5	3	135	7.6	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
HA-5127	Y	8.5	10	10	30	3.5	3	0.015	0.01	1	25	27	N	N	96	120	1	N	8 Ld SOIC
HA-5127A	Y	8.5	10	10	30	3.5	3	0.01	0.01	1	25	27	N	N	114	126	1	N	8 Ld CerDIP
HA-5137A	Y	63	20	10	40	3.5	3	0.01	0.01	1	25	27.6	N	N	114	126	5	N	8 Ld CerDIP
HA-5147		120	35	10	40	3.5	3.2	0.015	0.03	1	25	27	N	N	96	120	10	N	8 Ld CerDIP
ISL28191		61	17	3	5.5	3.5	1.7	6	0.63	1	130	4.97	N	Y	80	100	1	Y	6 Ld SOT-23 T+R, 6 Ld µTDFN T+R
ISL28291		61	17	3	5.5	3.5	1.7	6	0.63	2	130	4.97	N	Y	80	100	1	Y	10 Ld MSOP, 10 Ld µTQFN T+R, 8 Ld SOIC
CA3140		4.5	9	4	36	4	12	0.00001	5	1	40	27.4	N	N	80	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
CA3140A		4.5	9	4	36	4	12	0.00001	2	1	40	27.4	N	N	80	90	1	Strobe	8 Ld PDIP, 8 Ld SOIC
CA3240		4.5	9	4	36	4	12	0.00001	5	2	40	27.4	N	N	80	90	1	N	8 Ld PDIP
CA3240A		4.5	9	4	36	4	12	0.00001	2	2	40	27.4	N	N	80	90	1	N	8 Ld PDIP
EL5130	Y	500	350	5	12	4	1.8	2.3	0.9	1	100	7.6	N	N	90	110	5	Y	8 Ld SOIC
EL5131	Y	500	350	5	12	4	1.8	2.3	0.9	1	100	7.6	N	N	90	110	5	N	5 Ld SOT-23
EL5193	Y	300	2600	5	10	4	4.4	1	10	1	120	7.4	N	N	75	50	1	Y	5 Ld SOT-23 T+R, 6 Ld SOT-23 T+R
EL5193A		300	2600	5	10	4	4.4	1	10	1	120	7.4	N	N	75	50	1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
EL5197	Y	200	2200	5	10	4	4.4	1	10	1	120	7.4	N	N	75		1	Y	5 Ld SOT-23
EL5197A		200	2200	5	10	4	4.4	1	10	1	120	7.4	N	N	75		1	Y	6 Ld SOT-23 T+R, 8 Ld SOIC
EL5293	Y	300	2200	5	10	4	4.4	1	10	2	120	7.4	N	N	75	50	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5393A	Y	300	2200	5	10	4	4.4	1	10	3	120	7.4	N	N	75	50	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5397A		200	2100	5	10	4	4.8	1	10	3	120	7.4	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
HA-2520	Y	20	120	16	36	4	20	0.1	8	1	10	24	N	N	90	90	3	N	8 Ld Can, 8 Ld CerDIP
HA-2522	Y	20	120	16	36	4	20	0.125	10	1	10	24	N	N	90	90	3	N	8 Ld Can

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

I_S >1mA - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
HA-2525	Y	20	120	16	36	4	20	0.125	10	1	10	24	N	N	90	90	3	N	8 Ld Can, 8 Ld CerDIP, 8 Ld PDIP, 8 Ld SOIC
EL2126	Y	90	110	5	30	4.5	1.3	7	2	1	100	27.5	N	N	100	106	10	N	5 Ld SOT-23, 8 Ld SOIC
EL2228		80	65	5	24	4.5	4.9	4.5	3	2	180	20.6	N	N	83	90	1	N	8 Ld MSOP, 8 Ld SOIC
EL2227	Y	115	50	5	24	4.8	1.9	3.4	3	2	100	20.8	N	N	95	94	2	N	8 Ld MSOP, 8 Ld SOIC
HA-2404		40	30	20	40	4.8	20	0.05	4	4	20	24	N	N	90	100	1	Y	16 Ld CerDIP
EL1528		40	210	10	24	5	3.5	30	10	2	1000	22.2	N	N	75		1	Y	20 Ld HTSSOP, 24 Ld QFN
EL2250		125	300	2.7	12	5	48	5.5	12	2	100	10.8	N	N	70	70	1	N	8 Ld SOIC
EL5164	Y	600	4700	5	12	5	2.1	2	5	1	140	7.6	N	N	79	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5165	Y	600	4700	5	12	5	2.1	2	5	1	140	7.6	N	N	79	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5364	Y	600	4700	5	12	5	2.1	2	5	3	140	7.6	N	N	79	62	1	Y	16 Ld QSOP, 16 Ld SOIC
HA-5104	Y	8	3	5	18	5	4.3	130	0.5	4	15	26	N	N	100	95	1	N	14 Ld CerDIP, 16 Ld SOIC, 20 Ld LCC
EL2045	Y	100	275	4	36	5.2	15	2.8	7	1	75	27.2	N	N	85	95	2	N	8 Ld PDIP, 8 Ld SOIC
EL5102	Y	400	2200	5	10	5.2	12	2	5	1	150	7.8	N	N	80	80	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5103	Y	400	2200	5	10	5.2	12	2	5	1	150	7.8	N	N	80	80	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5202	Y	400	2200	5	10	5.2	12	2	5	2	150	7.8	N	N	80	80	1	Y	10 Ld MSOP
EL5203	Y	400	2200	5	10	5.2	12	2	5	2	150	7.8	N	N	80	80	1	N	8 Ld MSOP, 8 Ld SOIC
EL5302	Y	400	2200	5	10	5.2	12	2	5	3	150	7.8	N	N	80	80	1	Y	16 Ld QSOP
EL1516		350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	N	8 Ld MSOP, 8 Ld SOIC
EL1516A		350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	Y	10 Ld MSOP
HFA1155		360	1650	4.5	11	5.5	4.7	25	6	1	55	6.6	N	N	50	46	1	N	5 Ld SOT-23 T+R
EL8102	Y	500	600	3	5	5.6	12	6	8	1	65	4.8	N	Y	95	95	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL8103	Y	500	600	3	5	5.6	12	6	8	1	65	4.8	N	Y	95	95	1	N	5 Ld SOT-23
EL8202	Y	500	600	3	5.5	5.6	12	6	8	2	65	4.8	N	Y	95	95	1	Y	10 Ld MSOP
EL8203	Y	500	600	3	5.5	5.6	12	6	8	2	65	4.8	N	Y	95	95	1	N	8 Ld MSOP, 8 Ld SOIC
EL8302	Y	500	600	3	5.5	5.6	12	6	7	3	65	4.85	N	Y	95	95	1	Y	16 Ld QSOP, 16 Ld SOIC
EL8403	Y	500	600	3	5.5	5.6	12	6	8	4	65	4.8	N	Y	95	85	1	N	14 Ld SOIC, 16 Ld QSOP
HFA1105		330	1000	9	11	5.8	3.5	2	5	1	60	6.8	N	N	54	50	1	N	8 Ld SOIC
HFA1305		560	2500	9	11	5.8	3.5	6	5	3	60	6.6	N	N	52	48	1	N	14 Ld SOIC
HFA1405		560	2500	9	11	5.8	3.5	6	5	4	60	6.6	N	N	52	48	1	N	14 Ld PDIP, 14 Ld SOIC
HFA1145		330	1000	9	11	5.9	3.5	6	6	1	60	6.8	N	N	54	50	1	Y	8 Ld PDIP, 8 Ld SOIC
HFA1412		350	1650	9	11	5.9	7	1	10	4	55	6.4	N	N	49	45	1	N	14 Ld PDIP, 14 Ld SOIC
EL5156	Y	600	700	5	12	6	12	0.4	1	1	140	7.6	N	N	90	108	1	Y	8 Ld SOIC
EL5157	Y	600	700	5	12	6	12	0.4	1	1	140	7.6	N	N	90	108	1	N	5 Ld SOT-23

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

I_S >1mA - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL5172		250	800	5	10	6	26	6	25	1	95	7.5	N	N	58	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5192	Y	600	2800	5	10	6	4.1	3	10	1	120	7.4	N	N	75	50	1	Y	5 Ld SOT-23
EL5192A		600	2800	5	10	6	4.1	3	10	1	120	7.4	N	N	75	50	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5256	Y	600	700	5	12	6	12	0.02	1	2	140	7.6	N	N	90	108	1	Y	10 Ld MSOP
EL5257		600	700	5	12	6	12	0.02	1	2	140	7.6	N	N	90	108	1	N	8 Ld MSOP, 8 Ld SOIC
EL5292	Y	600	2300	5	10	6	4.1	4	10	2	120	7.4	N	N	75	50	1	Y	8 Ld SOIC
EL5372		250	800	5	10	6	26	6	25	3	95	7.5	N	N	58	95	1	Y	24 Ld QSOP
EL5392A	Y	600	2300	5	10	6	4.1	4	10	3	120	7.4	N	N	75	50	1	Y	16 Ld QSOP
HA-2510/883		12	50	20	40	6		0.2	8	1	65	20	N	N	80	80	1	N	8 Ld Can, 8 Ld CerDIP
ISL55191		800	260	3	5	6.1	1.3	12	0.8	1	132	4.98	N	Y	77	100	10	Y	8 Ld DFN, 8 Ld SOIC
ISL55291		800	260	3	5	6.1	1.3	12	0.8	2	132	4.98	N	Y	77	100	10	Y	10 Ld MSOP
EL5134	Y	650	450	5	12	6.7	1.5	3.7	1	1	140	7.8	N	N	85	108	5	Y	8 Ld SOIC
EL5135	Y	650	450	5	12	6.7	1.5	3.7	1	1	140	7.8	N	N	85	108	5	N	5 Ld SOT-23
EL5234	Y	650	450	5	12	6.7	1.5	3.7	1	2	140	7.8	N	N	85	108	5	Y	10 Ld MSOP
EL5235	Y	650	450	5	12	6.7	1.5	3.7	1	2	140	7.8	N	N	85	108	5	N	8 Ld SOIC
HFA1135		360	1200	9	11	6.9	3.5	6	5	1	60	6.8	N	N	54	50	1	N	8 Ld SOIC
EL1509		70	500	5	12	7	2.8	30	20	2	450	10	N	N	50		1	N	8 Ld DFN, 8 Ld SOIC
EL1517B		200	800	5	12	7	6	5	25	2	250	10	N	N	90		1	Y	10 Ld HMSOP, 16 Ld QFN, 8 Ld SOIC
EL5144		100	200	4.75	5.25	7	25	2	25	1	90	4.85	N	Y	60	50	1	N	5 Ld SOT-23
EL5146		100	200	4.75	5.25	7	25	2	25	1	90	4.85	N	Y	60	50	1	Y	8 Ld PDIP, 8 Ld SOIC
EL5244		100	200	4.75	5.25	7	25	2	25	2	90	4.85	N	Y	60	50	1	N	8 Ld MSOP, 8 Ld PDIP, 8 Ld SOIC
EL5246		100	200	4.75	5.25	7	25	2	25	2	90	4.85	N	Y	60		1	Y	10 Ld MSOP, 14 Ld PDIP, 14 Ld SOIC
EL5370		100	1100	5	10	7	30	6	25	3	85	6.9	N	N	83	84	2	Y	24 Ld QSOP
EL5444		100	200	4.75	5.25	7	25	2	15	4	90	4.85	N	Y	60	50	1	N	14 Ld PDIP, 14 Ld SOIC, 16 Ld QSOP
ISL55036		400	2500	3	5.5	7.2	12.5	5.5	10	6	95	4.86	N	Y	78		4	Y	24 Ld TQFN
EL5170		100	1100	5	10	7.4	30	6	25	1	85	6.9	N	N	83	84	2	Y	8 Ld MSOP, 8 Ld SOIC
EL1507		70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	16 Ld QFN, 16 Ld SOIC
EL1510		70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	62		1	N	8 Ld DFN, 8 Ld SOIC
EL1519		70	350	5	12	7.5	2.7	30	20	2	450	10	N	N	50		1	N	8 Ld SOIC
EL1527		70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	60		1	Y	28 Ld HTSSOP
EL1537		50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537A		50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537B		50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL5176		250	800	4.75	11	7.5	26	6	2.5	1	40	7.8	N	N	84	82	1	Y	10 Ld MSOP
HA5023	Y	125	475	9	30	7.5	4.5	4	3	2	20	6	N	N	60	53	1	N	8 Ld PDIP, 8 Ld SOIC
HA5024	Y	125	475	9	30	7.5	4.5	4	3	4	20	6	N	N	60	53	1	Y	20 Ld PDIP, 20 Ld SOIC
EL5171		250	800	5	10	8	26	6	25	1	90	6.8	N	N	84	82	1	N	8 Ld SOIC
EL5371		250	800	5	10	8	26	6	25	3	90	6.8	N	N	84	82	1	N	28 Ld QSOP

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

I_S >1mA - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
HA-5160		100	120	14	36	8	35	0.00002	3	1	20	22	N	N	86	80	1	N	8 Ld Can
EL5166	Y	1400	6000	5	12	8.5	1.7	8.5	5	1	160	7.6	N	N	50	57	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5167	Y	1400	6000	5	12	8.5	1.7	8.5	5	1	160	7.6	N	N	50	57	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5367		1000	6000	5	12	8.5	1.7	8.5	5	3	160	7.6	N	N	50	57	1	N	16 Ld QSOP
ISL55002		200	300	5	30	8.5	12	0.6	5	2	140	26.15	N	N	100	90	1	N	8 Ld SOIC
ISL55004		200	300	5	30	8.5	12	0.6	5	4	140	26.15	N	N	100	90	1	N	14 Ld SOIC
EL5191		1000	2800	5	10	9	3.8	40	15	1	120	7.4	N	N	75	50	1	Y	5 Ld SOT-23 T+R, 8 Ld SOIC
EL5191A		1000	2800	5	10	9	3.8	40	15	1	120	7.4	N	N	75	50	1	Y	8 Ld SOIC
EL5396A		400	2600	5	10	9	3.8	40	15	3	120	7.4	N	N	75		1	Y	16 Ld QSOP, 16 Ld SOIC
ISL55001		220	300	5	30	9	12	0.6	5	1	140	26.15	N	N	100	90	1	N	8 Ld SOIC
EL5104	Y	700	7000	5	10	9.5	10	8	10	1	160	7.6	N	N	70	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5105	Y	700	7000	5	10	9.5	10	8	10	1	160	7.6	N	N	70	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5204	Y	700	7000	5	10	9.5	10	8	10	2	160	7.6	N	N	70	62	1	Y	10 Ld MSOP
EL5205	Y	700	7000	5	10	9.5	10	8	10	2	160	7.6	N	N	70	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5304	Y	700	7000	5	10	9.5	10	8	10	3	160	7.6	N	N	70	62	1	Y	16 Ld QSOP
EL5175	Y	550	600	4.75	11	9.6	2	12.5	40	1	67	7.49	N	N	56	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5375		550	600	4.75	11	9.6	2	12.5	30	3	67	7.49	N	N	56	95	1	Y	24 Ld QSOP
EL2125	Y	175	185	5	30	10	0.83	22	2	1	100	27.1	N	N	97	106	10	N	5 Ld SOT-23, 8 Ld SOIC
HA-5020	Y	100	800	10	30	10	4.5	3	8	1	20	25.4	N	N	64	60	1	Y	20 Ld LCC, 8 Ld PDIP, 8 Ld SOIC
HFA1109	Y	450	1100	9	11	10	4	4	5	1	36	6.4	N	N	53	50	1	N	8 Ld SOIC
HA4600		480	1700	9	11	10.5		30	10	1	20	5.6	N	N			1	Y	6 Ld SOT-23, 8 Ld PDIP, 8 Ld SOIC, 8 Ld SOT-23 T+R
EL2141		150	800	6	12.6	11	36	6	40	1	60	7.2	N	N	70		2	N	8 Ld SOIC
EL2142		150	400	6	12.6	11	36	6	10	1	60	6.2	N	N	70	70	1	N	8 Ld SOIC
ISL28190		170	50	3	5.5	11	1	16	0.7	1	144	4.99	N	Y	80	103	1	Y	6 Ld SOT-23 T+R, 6 Ld µTDFN T+R
ISL28290		170	50	3	5.5	11	1	16	0.7	2	144	4.97	N	Y	80	103	1	Y	10 Ld MSOP, 10 Ld µTQFN T+R, 8 Ld SOIC
EL5132	Y	670	1000	5	12	12	0.9	12	1	1	140	7	N	N	87	100	10	Y	8 Ld SOIC
EL5133	Y	670	1000	5	12	12	0.9	12	1	1	140	7	N	N	87	100	10	N	5 Ld SOT-23
EL5373		450	1100	4.75	11	12	25	13	30	3	55	7.7	N	N	73	80	2	Y	24 Ld QSOP
EL1503A		80	1100	10	24	12.5	3.5	50	30	2	1000	21.2	Y	N	90		1	Y	20 Ld SOIC, 24 Ld QFN
EL5174		550	1100	4.75	11	12.5	21	14	25	1	60	7.6	N	N	75	78	1	N	8 Ld SOIC
EL5177		550	1100	4.75	11	12.5	21	14	25	1	50	7.6	N	N	75	78	1	Y	10 Ld MSOP
EL5178		700	850	4.75	11	12.5	18	14	30	1	100	7.4	N	N	75	78	2	N	8 Ld MSOP, 8 Ld SOIC
EL5374		550	1100	4.75	11	12.5	21	14	25	3	60	7.6	N	N	75	78	1	Y	28 Ld QSOP
EL5378		700	1000	4.75	11	12.5	18	14	30	3	60	7.4	N	N	75	78	2	Y	28 Ld QSOP

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

I_S >1mA - Amplifiers (Continued)

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ISL1535		100	500	5	30	13.3	3.9	70		2	500	21.6	N	N	50		5	Y	24 Ld QFN, 28 Ld HTSSOP
EL8108		300	1100	4.5	13	14.3	6	5	25	2	450	10	N	N	90		1	Y	16 Ld QFN, 8 Ld SOIC
EL4543		350	1200	5	12	14.5	27	15	10	3	60		N	N	80	80	2	Y	Eval Board, 20 Ld QFN, 24 Ld QSOP
ISL1557		300	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1557A		300	1200	4.5	13.2	15	8.2	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1571		250	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95		1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1572		250	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95		1	Y	10 Ld HMSOP, 16 Ld QFN
ISL55190		800	268	3	5	16	1.2	25	0.5	1	130	4.97	N	Y	100	95	5	Y	8 Ld DFN, 8 Ld SOIC
ISL55290		800	268	3	5	16	1.2	25	0.5	2	130	4.97	N	Y	100	95	5	Y	10 Ld MSOP
ISL59311		650	1500	4.5	5.5	17	15	6	1	3	60	4.4	N	N	75	75	2	Y	32 Ld QFN
HA-2539	Y	600	600	20	30	20	6	5	15	1	20	20	N	N	70	72	10	N	14 Ld PDIP
HA-2540	Y	400	400	20	30	20	6	5	8	1	20	20	N	N	70	72	10	N	14 Ld CerDIP
HA-5033	Y	250	1100	10	32	21	20	20	5	1	100	22	N	N	54			N	12 Ld Can, 8 Ld PDIP
HFA1100	Y	850	2300	4.5	11	21	4	2.5	6	1	60	6.6	N	N	50	46	1	N	8 Ld PDIP, 8 Ld SOIC
HFA1112		850	2400	9	11	21	9	25	25	1	60	6.6	N	N	45		1	N	8 Ld PDIP, 8 Ld SOIC
HFA1113		850	2400	9	11	21	9	25	25	1	60	6.6	N	N	45		1	N	8 Ld SOIC
HFA1130		850	2300	9	11	21	4	25	6	1	60	6.6	N	N	50	46	1	N	20 Ld LCC, 8 Ld SOIC
ISL55020		300	1200	4.5	12	21	6.9	7	38	1	150	10	N	N	60		1	Y	16 Ld QFN T+R
HFA1110		750	1300	9	11	26	14	10	25	1	60	6.6	N	N	46	60	1	N	8 Ld SOIC
ISL59833		200	500	3	3.6	32.3	20		25	3	50	5.1	Y	Y	68		2	Y	16 Ld QSOP
ISL59837		200	500	3	3.6	32.3	20		25	3	50	5.1	Y	Y	62		2	Y	16 Ld QSOP
EL9110		150	1500	10	12	33		1	250	1	60	7	N	N	60	60	1	Y	16 Ld QSOP
ISL59830		200	500	3	3.6	40	20		25	3	50	5.1	Y	Y	90		2	Y	16 Ld QSOP
ISL59830A		200	500	3	3.6	40	20		25	3	50	5.1	Y	Y	90		2	Y	16 Ld QSOP

www.BDTIC.com/Intersil

I_S >1mA - Buffers - General

Device	iSim	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	Gain A_V (min)	Enable	Package
HA-5002	Y	110	1300	10	40	8.3	2	5	1	200	27	N	N	64	0.995	N	20 Ld PLCC, 8 Ld Can, 8 Ld PDIP, 8 Ld SOIC

I_S >1mA - Buffers - Programmable Gamma Reference Generators

Device	BW (MHz)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	Enable	Package
EL5825	5	4.5	16.5	8	8	140	14.9	N	Y	60	Y	24 Ld QFN, 24 Ld TSSOP
EL5225		5	16.5	9	10	140	14.9	N	Y	65	N	24 Ld TSSOP
EL5126	0.4	4.5	16.5	10	8	240	14.9	N	Y	60		32 Ld QFN

 Copyright © 2008 Intersil Americas Inc. All rights reserved.

Fully Differential Wideband Amplifier

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
ISL55020	300	1200	4.5	12	21	6.9	7	38	1	150	10	N	N	60		1	Y	16 Ld QFN T+R
ISL59311	650	1500	4.5	5.5	13	15	6	1	3	60	4.4	N	N	75	75	2	Y	32 Ld QFN
ISL59830	200	500	3	3.6	40	20		25	3	50	5.1	Y	Y	90		2	Y	16 Ld QSOP
ISL59830A	200	500	3	3.6	40	20		25	3	50	5.1	Y	Y	90		2	Y	16 Ld QSOP
ISL59833	200	500	3	3.6	32.3	20		25	3	50	5.1	Y	Y	68		2	Y	16 Ld QSOP
ISL59837	200	500	3	3.6	32.3	20		25	3	50	5.1	Y	Y	62		2	Y	16 Ld QSOP

RF Amplifiers

Device	BW (MHz)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Frequency Range (MHz)	S11 @ 1GHz (-dB)	S12 @ 1GHz (-dB)	S21 @ 1GHz (dB)	S22 @ 1GHz (-dB)	OIP3 @ 1GHz (dBm)	Z_{IN} (Ω)	Z_{OUT} (Ω)	NF @ 2GHz (dB)	Package
ISL55005	3000	3.3	3.3	20.6	500 to 3000	14.5	21.8	17.2	17.6	18	75	50	3.6	6 Ld SC-70 T+R
ISL55007	3300	3.3	3.3	23.5	500 to 3000	16.4	21	15.7	12.9	18	75	75	3.8	6 Ld SC-70 T+R
ISL55008	3100	3.3	3.3	20.8	500 to 3000	15	20.2	15.3	15.6	18.6	50	50	3.9	6 Ld SC-70 T+R
ISL55009	3200	3.3	3.3	13.7	500 to 3000	11.1	21	15.6	13.6	10.6	75	50	3.6	6 Ld SC-70 T+R
ISL55010	3400	3.3	3.3	13.6	500 to 3000	13.3	20.6	14.8	12.5	11.2	75	75	3.7	6 Ld SC-70 T+R
ISL55011	3400	3.3	3.3	13.7	500 to 3000	11.1	19.6	14.3	13.5	10.9	50	50	3.9	6 Ld SC-70 T+R
ISL55012	2400	3	5.5	63.5	500 to 3000	23.5	22.4	18.3	21.8	30	75	50	4.7	6 Ld SC-70 T+R
ISL55013	2800	3	5.5	62.3	500 to 3000	15.8	17.8	11.8	15.9	31.5	50	50	5	6 Ld SC-70 T+R
ISL55014	2750	3	5.5	63	500 to 3000	20.8	22.2	17.2	12.8	30.3	50	50	4.3	6 Ld SC-70 T+R
ISL55015	2900	3	5.5	62.5	500 to 3000	20.2	18.9	13.5	21.4	31.3	75	50	4.8	6 Ld SC-70 T+R

www.BDTIC.com/Intersil

Device	BW (MHz)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Frequency Range (MHz)	S11 @ 1GHz Single-Ended (-dB)	S12 @ 1GHz Differential (-dB)	S21 @ 1GHz Differential (dB)	S22 @ 1GHz Differential (-dB)	OIP3 @ 1GHz Differential (dBm)	Z_{IN} (Ω)	Z_{OUT} (Ω)	NF @ 1GHz (dB)	Package
ISL55016	3000	4.5	5.5	104	50 to 3000	15.1	30.2	17.1	10.6	26.2	75	100	5.4	6 Ld TDFN T+R

High Output Current Amplifiers

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL1516	350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	N	8 Ld MSOP, 8 Ld SOIC
EL1516A	350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	Y	10 Ld MSOP
EL1517B	200	800	5	12	7	6	5	25	2	250	10	N	N	90		1	Y	10 Ld HMSOP, 16 Ld QFN, 8 Ld SOIC
EL1509	70	500	5	12	7	2.8	30	20	2	450	10	N	N	50		1	N	8 Ld DFN, 8 Ld SOIC
EL1511	70	500	5	15	3.5	2.8	30	20	2	450	13	N	N	50		1	Y	16 Ld QFN, 16 Ld SOIC
EL1519	70	350	5	12	7.5	2.7	30	20	2	450	10	N	N	50		1	N	8 Ld SOIC
EL1506	70	500	5	24	3.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	24 Ld SOIC, 28 Ld HTSSOP
EL1507	70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	16 Ld QFN, 16 Ld SOIC
EL1510	70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	62		1	N	8 Ld DFN, 8 Ld SOIC

 Copyright © 2008 Intersil Americas Inc. All rights reserved.

High Output Current Amplifiers (Continued)

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL1527	70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	60		1	Y	28 Ld HTSSOP
EL1537	50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537A	50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537B	50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
ISL1535	100	500	5	30	13.3	3.9	70		2	500	21.6	N	N	50		5	Y	24 Ld QFN, 28 Ld HTSSOP
EL1503A	80	1100	10	24	12.5	3.5	50	30	2	1000	21.2	Y	N	90		1	Y	20 Ld SOIC, 24 Ld QFN
EL1508	80	600	5	12	3.5	3.5	50	10	2	1000	21.6	N	N			1	Y	16 Ld SOIC, 20 Ld SOIC, 24 Ld QFN
EL1526	46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	20 Ld HTSSOP, 24 Ld QFN
EL1528	40	210	10	24	5	3.5	30	10	2	1000	22.2	N	N	75		1	Y	20 Ld HTSSOP, 24 Ld QFN
EL1529	46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	24 Ld QFN
ISL1557	300	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1557A	300	1200	4.5	13.2	15	8.2	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1571	250	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95		1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1572	250	1200	4.5	13.	15	6	3	7.5	2	1000	10	N	N	95		1	Y	0 Ld HMSOP, 6 Ld QFN

www.BDTIC.com/Intersil

Comparators

Precision

Device	# of Devices/ Channels	Response Time (ns)	I_S (mA)	Input Supply Range (V_P)	Max Input Signal (V)	Comparator Receiver Range (V)	Output Signal Range (V)	Max Output Signal (V)	I_{BIAS} (nA)	V_{OS} (max) (mV)	I_{OS} (nA)	Package
HA-4900	4	130	6.5	±15	(V+) - 2.4	V- to (V+) - 2.4	0.2 to 4.2	4.2	50	3	10	16 Ld CerDIP
HA-4902	4	130	6.5	±15	(V+) - 2.6	V- to (V+) - 2.6	0.2 to 4.2	4.2	50	5	10	16 Ld CerDIP
HA-4905	4	130	7	±15	(V+) - 2.4	V- to (V+) - 2.4	0.2 to 4.2	4.2	100	7.5	25	16 Ld CerDIP, 16 Ld SOIC

Ultra-Small

Device	# of Devices/ Channels	Response Time (ns)	I_S (mA)	Input Supply Range (V_P)	Max Input Signal (V)	Comparator Receiver Range (V)	Output Signal Range (V)	Max Output Signal (V)	I_{BIAS} (nA)	V_{OS} (max) (mV)	I_{OS} (nA)	Package
ISL28196	1	150000	0.004	1.8 to 5.5	V+	RRI	RRO	V+	0.08	2	0.06	6 Ld SOT-23 T+R
ISL28197	1	600000	0.0016	1.8 to 5.5	V+	RRI	RRO	V+	0.08	2	0.06	6 Ld SOT-23 T+R

Instrumentation Amplifiers

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail Input	Rail-to-Rail Output	PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
EL8171	0.45	0.5	2.9	5	0.078	200	0.00001	1	1	29	5.5	Y	Y	90	104	10	Y	8 Ld SOIC
EL8173	0.396	0.5	2.9	5	0.078	200	0.0007	1	1	29	5.5	Y	Y	90	104	10	Y	8 Ld SOIC
ISL28270	0.24	0.5	2.4	5.5	0.06	60	0.0005	0.15	2	29	4.99	Y	Y	110	110	100	Y	16 Ld QSOP
ISL28470	0.24	0.5	2.4	5.5	0.065	60	0.00025	0.015	4	29	4.99	Y	Y	110	110	100	Y	28 Ld QSOP

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

Instrumentation Amplifiers (Continued)

Device	BW (MHz)	SR (V/µs)	V_S (min) (V)	V_S (max) (V)	I_S (per amp) (mA)	Noise V_N (nV/√Hz)	I_{BIAS} (µA)	V_{OS} (max) (mV)	# of Devices/ Channels	I_{OUT} (mA)	V_{OUT} (V)	Rail-to-Rail		PSRR (dB)	CMRR (dB)	Gain A_V (min)	Enable	Package
												Input	Output					
EL8170	0.192	0.5	2.9	5	0.078	50	0.0007	0.25	1	29	5.5	Y	Y	104	108	100	Y	8 Ld SOIC
ISL28271	0.18	0.5	2.4	5	0.078	240	0.00001	0.6	2	31	5.5	Y	Y	100	100	10	Y	16 Ld QSOP
EL8172	0.172	0.5	2.9	5	0.078	70	0.00001	0.3	1	29	5.5	Y	Y	104	108	100	Y	8 Ld SOIC
ISL28272	0.1	0.5	2.4	5	0.078	78	0.00001	0.5	2	31	5.5	Y	Y	100	100	100	Y	15 Ld QSOP T+R, 16 Ld QSOP

Device	# of Devices/ Channels	INA BW @ -3dB or GBWP (MHz)	OPA BW @ -3dB or GBWP (MHz)	Slew Rate (V/µs)		V_S (V)		Noise V_N (nV/√Hz)		Rail-to-Rail	Gain A_V (min)		I_S (mA)	I_{BIAS} (µA)	I_{OUT} (mA)	V_{OS} (max) (mV)		INA Gain Error (%)	Enable	CMRR (dB)	PSRR (dB)	Package
				INA	OPA	Min	Max	INA	OPA		INA	OPA				INA	OPA					
ISL28274	1	2.5	0.3	±0.5	±0.14	2.4	5	210	50	RRIO	100	1	0.078	0.00003	31	0.4	0.225	0.2	Y	100	100	16 Ld QSOP
ISL28474	2	2.5	0.3	±0.5	±0.14	2.4	5	210	50	RRIO	100	1	0.078	0.00003	31	0.4	0.225	0.2	Y	100	100	24 Ld QSOP

Transistor Arrays

NPN

Device	NPN or PNP	# of Devices/ Channels	V(BR) CBO	V(BR) CEO (V)	V(BR) EBO (V)	ICEO (nA)	ICBO (nA)	HFE	NF (dB)	FT (GHz)	V_{OS} (max) (mV)	I_{OS} (µA)	CEB (pF)	CCB (pF)	Package
CA3054	NPN	2	60	24	7	N/A	0.002	N/A	8	0.55	5	0.3	N/A	N/A	14 Ld SOIC
CA3083	NPN	5	60	24	7	0.07	0.02	76	N/A	0.45	5	0.7	N/A	N/A	16 Ld PDIP, 16 Ld SOIC
CA3102	NPN	2	60	24	7	N/A	0.0013	N/A	1.5	1.35	5	0.3	N/A	0.28	14 Ld SOIC
CA3127	NPN	5	32	24	6	0.01	0.005	90	2.2	1.15	5	N/A	0.6	0.2	16 Ld SOIC
HFA3046	NPN	5	18	12	6	2	0.1	70	3.5	8	5	5	0.5	0.5	14 Ld SOIC
HFA3102	NPN	2	18	12	6	N/A	0.1	70	2.1	10	5	5	0.2	0.3	14 Ld SOIC
HFA3127	NPN	5	18	12	6	2	0.1	70	3.5	8	5	N/A	0.5	0.5	16 Ld QFN, 16 Ld SOIC
HFA3134	NPN	2	21	9	6	1.1	0.2	100	2.4	8.5	6	N/A	0.6	0.5	6 Ld SOT-23 T+R

www.BDTIC.com/Intersil

PNP

Device	NPN or PNP	# of Devices/ Channels	V(BR) CBO	V(BR) CEO (V)	V(BR) EBO (V)	ICEO (nA)	ICBO (nA)	HFE	NF (dB)	FT (GHz)	V_{OS} (max) (mV)	I_{OS} (µA)	CEB (pF)	CCB (pF)	Package
HFA3128	PNP	5	15	15	5	2	0.1	35	5.5	5.5	5	N/A	0.5	0.6	16 Ld QFN, 16 Ld SOIC
HFA3135	PNP	2	21	14	5	0.25	0.5	57	4.6	7	6	N/A	0.55	0.4	6 Ld SOT-23 T+R

NPN/PNP

Device	NPN or PNP	# of Devices/ Channels	V(BR) CBO	V(BR) CEO (V)	V(BR) EBO (V)	ICEO (nA)	ICBO (nA)	HFE	NF (dB)	FT (GHz)	V_{OS} (max) (mV)	I_{OS} (µA)	CEB (pF)	CCB (pF)	Package
HFA3096	NPN/PNP	3/2	18/15	12/15	6/5	2-Feb	0.1/0.1	70/35	3.5/3.5	8/5.5	5	N/A	0.5/0.5	0.5/0.6	16 Ld SOIC

Gilbert Cell

Device	BW (MHz)	SR (V/µs)	V_{CC} Range (±V)	Package
HFA3101	900	N/A	3V to 5V Single Supply	8 Ld SOIC

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

V_{COM} Amplifiers

By Bandwidth

Bandwidth >100MHz

Device	# of Devices/ Channels	V_S (V)	BW (MHz)	SR (V/µs)	Rail-to-Rail	I_S (mA)	I_{OUT} (mA)	Package
EL9211	1	4.5 to 14	130	115	In	2.4	300	5 Ld SOT-23, 8 Ld HMSOP
EL9212	2	4.5 to 14	130	115	In	2.4	300	8 Ld HMSOP

Bandwidth >10MHz - V_{COM} Amplifiers

Device	# of Devices/ Channels	V_S (V)	BW (MHz)	SR (V/µs)	Rail-to-Rail	I_S (mA)	I_{OUT} (mA)	Package
EL5111	1	4.5 to 18	60	75	In/Out	2.5	180	5 Ld TSOT, 8 Ld HMSOP
EL5211	2	4.5 to 18	60	75	In/Out	2.5	180	8 Ld HMSOP
EL5211A	2	4.5 to 18	60	75	In/Out	2.5	180	8 Ld HMSOP
EL5411	4	4.5 to 18	60	75	In/Out	2.5	180	14 Ld HTSSOP, 14 Ld TSSOP
EL5611	6	4.5 to 18	60	75	In/Out	2.5	180	24 Ld HTSSOP
EL5811	8	4.5 to 18	60	75	In/Out	2.5	180	28 Ld HTSSOP
EL5210	2	4.5 to 18	30	33	In/Out	2.5	120	8 Ld MSOP, 8 Ld SOIC
EL5410	4	4.5 to 18	30	33	In/Out	2.5	120	14 Ld SOIC, 14 Ld TSSOP
EL5120	1	4.5 to 18	12	10	In/Out	0.5	30	5 Ld TSOT, 8 Ld HMSOP
EL5220	2	4.5 to 18	12	10	In/Out	0.5	30	8 Ld MSOP
EL5420	4	4.5 to 18	12	10	In/Out	0.5	30	14 Ld SOIC, 14 Ld TSSOP, 16 Ld QFN

www.BDTIC.com/Intersil

Bandwidth >10MHz - Programmable V_{COM} Amplifiers

Device	# of DCP Channels	# of V_{COM}	V_S (V)	BW (MHz)	SR (V/µs)	EEPROM	Rail-to-Rail	Interface	Package
EL9200	1	1	4.5 to 16.5	44	10	Y	Y	Up/Down	12 Ld DFN
EL9201	1	2	4.5 to 16.5	44	10	Y	Y	Up/Down	24 Ld QFN
EL9202	1	4	4.5 to 16.5	44	10	Y	Y	Up/Down	24 Ld QFN

Bandwidth >10MHz - Integrated Gamma Buffers with V_{COM} Amplifiers

Device	# of Buffers	# of V_{COM}	V_S (V)	V_{COM} BW (MHz)	BW (MHz)	V_{COM} I_{SC} (mA)	I_S (mA)	Package
EL5524	4	1	4.5 to 18	60	12	140	4.9	14 Ld HTSSOP
EL5624	6	1	4.5 to 18	60	12	140	6.1	20 Ld HTSSOP
EL5724	8	1	4.5 to 18	60	12	140	7.3	24 Ld HTSSOP
EL5824	10	1	4.5 to 18	60	12	140	8.5	28 Ld HTSSOP
EL5128	1	2	4.5 to 18	12	12	120	1.5	10 Ld MSOP

Bandwidth <10MHz - Integrated Gamma Buffers with V_{COM} Amplifiers

Device	# of Buffers	# of V_{COM}	V_S (V)	V_{COM} BW (MHz)	BW (MHz)	V_{COM} I_{SC} (mA)	I_S (mA)	Package
EL5224	8	1	4.5 to 18	1.2	12	160	5.8	24 Ld HTSSOP, 24 Ld QFN
EL5324	10	1	4.5 to 18	1.2	12	160	7	28 Ld HTSSOP, 32 Ld QFN
EL5424	12	1	4.5 to 18	1.2	12	160	8.2	32 Ld QFN

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.

By Supply Current

I_S >1mA

Device	# of Buffers	# of V_{COM}	V_S (V)	V_{COM} BW (MHz)	BW (MHz)	V_{COM} I_{SC} (mA)	I_S (mA)	Package
EL5120	1	4.5 to 18	12	10	In/Out	0.5	30	5 Ld TSOT, 8 Ld HMSOP
EL5220	2	4.5 to 18	12	10	In/Out	0.5	30	8 Ld MSOP
EL5420	4	4.5 to 18	12	10	In/Out	0.5	30	14 Ld SOIC, 14 Ld TSSOP, 16 Ld QFN

I_S >1mA - V_{COM} Amplifiers

Device	# of Buffers	# of V_{COM}	V_S (V)	V_{COM} BW (MHz)	BW (MHz)	V_{COM} I_{SC} (mA)	I_S (mA)	Package
EL5210	2	4.5 to 18	30	33	In/Out	2.5	120	8 Ld MSOP, 8 Ld SOIC
EL5410	4	4.5 to 18	30	33	In/Out	2.5	120	14 Ld SOIC, 14 Ld TSSOP
EL5111	1	4.5 to 18	60	75	In/Out	2.5	180	5 Ld TSOT, 8 Ld HMSOP
EL5211	2	4.5 to 18	60	75	In/Out	2.5	180	8 Ld HMSOP
EL5411	4	4.5 to 18	60	75	In/Out	2.5	180	14 Ld HTSSOP, 14 Ld TSSOP
EL5611	6	4.5 to 18	60	75	In/Out	2.5	180	24 Ld HTSSOP
EL5811	8	4.5 to 18	60	75	In/Out	2.5	180	28 Ld HTSSOP
EL9211	1	4.5 to 14	130	115	In	2.4	300	5 Ld SOT-23, 8 Ld HMSOP
EL9212	2	4.5 to 14	130	115	In	2.4	300	8 Ld HMSOP

I_S >1mA - Integrated Gamma Buffers with V_{COM} Amplifiers

Device	# of Buffers	# of V_{COM}	V_S (V)	V_{COM} BW (MHz)	BW (MHz)	V_{COM} I_{SC} (mA)	I_S (mA)	Package
EL5128	1	2	4.5 to 18	1.2	12	160	1.8	10 Ld MSOP
EL5524	4	1	4.5 to 18	60	12	140	4.9	14 Ld HTSSOP
EL5224	8	1	4.5 to 18	1.2	12	160	5.8	24 Ld HTSSOP, 24 Ld QFN
EL5624	6	1	4.5 to 18	60	12	140	6.1	20 Ld HTSSOP
EL5324	10	1	4.5 to 18	1.2	12	160	7	28 Ld HTSSOP, 32 Ld QFN
EL5724	8	1	4.5 to 18	60	12	140	7.3	24 Ld HTSSOP
EL5424	12	1	4.5 to 18	1.2	12	160	8.2	32 Ld QFN
EL5824	10	1	4.5 to 18	60	12	140	8.5	28 Ld HTSSOP

www.BDTIC.com/Intersil

1-888-INTERSIL or 1-888-468-3774 | Copyright © 2008 Intersil Americas Inc. All rights reserved.